DEVKIT-MPC5748G QUICK START GUIDE (QSG)

Ultra-Reliable MCUs for Industrial and Automotive Applications

www.nxp.com/DEVKIT-MPC5748G





SECURE CONNECTIONS FOR A SMARTER WORLD

EXTERNAL USE

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Quick Start Package Overview

Board:

DEVKIT-MPC5748G Low cost EVB with MPC5748G(176 LQFP-EP) Auto quality MCU on board

Documents:

Name	Description
Quick Start Guide(QSG)	Detailed description on availability of Hardware, Software and Documents to quick start with MPC5748G project (this document)
Software Integration Guide(SWIG)	Detailed walk through on how to install and use S32 Design Studio IDE for Power Architecture
Application Notes	Detailed documents covering topics from 'how to design hardware' to 'how to write software'
Fact Sheets, Reference Manuals and Data Sheets	Detailed manuals for MPC5748G family of MCU and DEVKIT-MPC5748G board

Downloads:

Name	Description
Integrated Development Environment (IDE)	Eclipse based S32DS IDE with free GCC compiler and Debugger support
DEVKIT-MPC5748G Quick Start Package	All in one package: Software examples and supporting documents for getting started
DEVKIT-MPC5748G Schematics	PDF schematic files for the DEVKIT-MPC5748G board
DEVKIT-MPC5748G PCB Design Package	Gerber files and Bill of Material



Step-by-Step Installation Instructions

In this quick start guide, you will learn how to set up the **DEVKIT-MPC5748G** board and run the default program.



	Install Software and Tools
1	Install S32 Design Studio IDE for Power Architecture.
	See Software Installation Guide (SWIG) for detailed procedure

Connect the USB Cable

2

3

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Connect one end of the USB cable to the PC and the other end to the micro-B connector on the DEVKIT-MPC5748G board. Allow the PC to automatically configure the USB drivers.

Observe the Default Program reaction

The pre-loaded example project utilizes the **DEVKIT-MPC5748G** user push button (SW2) and the user LEDs. Once the board is plugged in, LEDs will blink in default pattern. Press push button to switch between the two different patterns. Use Potentiometer to change blinking speed. (Clock is configured to PLL running at 160 MHz)

Learn More About the DEVKIT-MPC5748G

Read release notes and documentation on the nxp.com/DEVKIT-MPC5748G nxp.com/MPC5748G



DEVKIT-MPC5748G Board : Features

- MPC5748G has 2 x 160 MHz Power Architecture® e200Z4 Dual issue cores and 1 x 80 MHz Power Architecture® e200Z2 Single issue core
- MPC5748G qualified to AEC-Q100 Grade 1 and ambient temperature of -40 to +125 °C
- Arduino™ UNO R3 footprint-compatible with expansion "shield" support
- Integrated Open-standard Serial and Debug Adapter (OpenSDA) with support for several industry-standard debug interfaces as well as JTAG connector
- · Easy access to the MCU I/O header pins for prototyping
- On-chip connectivity for Ethernet, FlexRay, USB, CAN, LIN, UART/SCI and SPI
- Potentiometer for precise voltage and analog measurement
- 8 user LED
- 2 user push-button switches
- Flexible power supply options
 - micro-B USB
 - 12V External power supply
- Similar hardware across ARM[®], S12 and Power Architecture[®] architecture based MCUs
- DEVKIT-COMM shield boards for extended 4 CAN and 6 LIN ports





- Box includes:
 - DEVKIT-MPC5748G Board
 - USB Cable
- Downloads includes:
 - Quick Start Package
 - S32 Design Studio IDE
 - Application notes



DEVKIT-MPC5748G Board : Overview

The DEVKIT-MPC5748G is an ultra-low-cost development platform for MPC5748G Microcontrollers.

Features include easy access to all MCU I/Os, a standard-based form factor compatible with the Arduino[™] pin layout, providing a broad range of expansion board options, and a USB serial port interface for connection to the IDE. The board has option to be powered via USB or an external power supply.



DEVKIT-MPC5748G Board : Pinout



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EXTERNAL USE

			J.J.			
FUNCTION	PORT	PIN		PIN	PORT	FUNCTIO
SCL1	PI1	J3-19		J3-20	PG9	
SDA1	PIO	J3-17		J3-18	PG8	
	NC	J3-15		J3-16	PG7	
	GND	J3-13		J3-14	PG6	
SCLK_4	PF2	J3-11		J3-12	PI7	
SIN_4	PF1	J3-09		J3-10	PI6	
SOUT_4	PFO	J3-07		J3-08	PA15	
SS_4	PF3	J3-05		J3-06	PA14	
Timer	PA2	J3-03		J3-04	PA13	
	PA1	J3-01		J3-02	PA12	

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J2

FUNCTION	PORT	PIN
	PB15	J2-15
Timer	PB14	J2-13
Timer	PB13	J2-11
	PB12	J2-09
Timer	PB11	J2-07
	PB10	J2-05
LIN4TX(UART)	PA5	J2-03
LIN4RX(UART)	PA6	J2-01

Arduino Compatibility The internal rows of the I/O headers on the DEVKIT-MPC5748G are arranged to fulfill Arduino[™] shields compatibility.



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DEVKIT-MPC5748G Board : Pinout



EXTERNAL USE

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			J13			
FUNCTION	PORT	PIN		PIN	PORT	FUNCTION
	PF11	J13-02		J13-01	VIN	12V
	PF10	J13-04		J13-03	PER_HVA	
	PF6	J13-06		J13-05	RESET	MPC5748G Reset
	PF4	J13-08		J13-07	3V3_SR	3.3V
	PF5	J13-10		J13-9	5V0_SR	5V
	PF7	J13-12		J13-11	GND	
	PF8	J13-14	1 1 1	J13-13	GND	
	PF9	J13-16		J13-15	VIN	12V

J14

FUNCTION	PORT	PIN
	PB4	J14-02
	PB5	J14-04
	PB6	J14-06
	PB7	J14-08
	PD0	J14-10
	PD1	J14-12
	PD2	J14-14
	PD3	J14-16

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DEVKIT-MPC5748G Board : Pinout





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EXTERNAL USE

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FUNCTION	PORT	PIN		PORT	FUN
	PF12	J4-19	J4-20	PE5	
	PF13	J4-17	J4-18	B PE4	
	PC6	J4-15	J4-16	6 PEO	
	PC7	J4-13	J4-14	PE1	
	PI2	J4-11	J4-12	GND	
	PI3	J4-09	J4-10	PER_HVA	
	PH3	J4-07	J4-08	B PC11	
	PH4	J4-05	J4-06	6 PC10	
	PG3	J4-03	J4-04	PG5	
	PG2	J4-01	J4-02	PG4	

			112			
FUNCTION	PORT	PIN	JIZ	PIN	PORT	FUNCTION
	PD12	J12-2		J12-1	PD15	
	PD10	J12-4		J12-3	PD14	
	PD9	J12-6		J12-5	PD13	
	PD8	J12-8		J12-7	PH8	
	PER_HVA	J12-10		J12-9	PH7	
	GND	J12-12		J12-11	PH6	
	PD7	J12-14		J12-13	PJ3	
	PD6	J12-16		J12-15	PJ2	
	PD5	J12-18		J12-17	PJ1	
	PD4	J12-20		J12-19	PJ0	

Arduino Compatibility The internal rows of the I/O headers on the DEVKIT-MPC5748G are arranged to fulfill Arduino[™] shields compatibility.



DEVKIT-MPC5748G Board : Jumper Settings

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There is only one jumper: J11 It is to select Power source:

> 1-2: **Default** External 12 V Supply 2-3: USB powered 5V Supply, through OpenSDA interface



Note: For high power/current consuming applications (like using external shield boards) use "External 12 V Supply" only



DEVKIT-MPC5748G Board : Communication Interfaces 1 of 2





Note: LIN Physical Interface require 12V supply on pin P3-03

DEVKIT-MPC5748G Board : Communication Interfaces

2 of 2

Understrate Use Use <th< th=""><th></th><th>UART_2 – Lir</th><th>าFlex_2</th><th></th><th></th></th<>		UART_2 – Lir	าFlex_2		
DESCRIPTION PORT DESCRIPTION PORT TX PC8 ULPI1_D7 PH12 RX PC9 ULPI1_06 PH11 ULP1_05 PG10 ULP1_05 PG10 ULP1_04 PG30 ULP1_05 PG14 ULP1_00 PG14 ULP1_01 PG35 ULP1_02 PE14 ULP1_00 PG14 ULP1_01 PG35 ULP1_02 PG14 ULP1_01 PG35 ULP1_101 PG35 ULP1_101 PG35 ULP1_102 PG14 ULP1_101 PG35 ULP1_101 PG35 ULP1_101 PG3 ULP1_101 PG3 ULP1_101 PG3 ULP1_101 PG3 ULP1_102 PG3 ULP1_103 PG3 ULP1_104 PG3 ULP1_105 PG3 ULP1_105 PG3		OpenSDA mi	cro-USB	USB_1	
TX PC8 ULP11_D7 PH12 RX PC9 ULP11_D6 PH11 ULP12_D5 PG11 ULP12_D5 PG11 ULP12_D2 PE14 ULP12_D2 PE14 ULP11_D1 PG15 ULP12_D1 PG15 ULP12_D2 PE14 ULP12_D1 PG15 ULP12_D1 D0 PG15 ULP12_D1 ULP12_D1 D1 PG15 ULP12_D1 ULP12_CK PC2 E E NUM1_O_TX_EN PC2 E E NMI_0_RX_EN PH12 PH12 PH12 RMI_0_RX_EN PF15 RMI_0_RX_EN PF15 RMI_0_MDC PG0 PG1 PG1		DESCRIPTION	PORT	DESCRIPTION	PORT
RX PC9 ULPI1_D6 PH11 ULPI1_D5 PG10 ULPI1_D3 PE15 ULPI1_D1 PG15 ULPI1_D2 PE14 ULPI1_D1 PG15 ULPI1_D2 PE14 ULPI1_D1 PG15 RMII_0_TXEI0 PH1 RMII_0_RXE		ΤХ	PC8	ULPI1_D7	PH12
VURIT PG10 VUP1_DS PE15 ULP1_D1 PG15 ULP1_D1 PG15 ULP1_D1 PG16 ULP1_D1 PG15 ULP1_D2 PE14 ULP1_D1 PG15 ULP1_D2 PE14 ULP1_D1 PG15 ULP1_D2 PE14 ULP1_D1 PG23 ULP1_D1 PG24 ULP1_D1 PC2 ULP1_D1 PC3 ULP1_D1 PC3 ULP1_D1 PC3 ULP1_D1 PC3 ULP1_D1 PC4 ULP1_D1 PC3 ULP1_D1 PC3 ULP1_D1 PC4 ULP1_D1 PC3 ULP1_D1 PC4 ULP1_D1 PC3 ULP1_D1 PC3 ULP1_D1 PC4 ULP1_D1 PC4 ULP1_D1 PC4 ULP1_D1 PC4 ULP1_D1 PC4 RMI_0_RXD1 PA8 RMI_0_RXD1 PA8		RX	PC9	ULPI1_D6	PH11
uerret UUPI1_D4 PG10 UUPI1_D2 PE14 UUPI1_D1 PG15 UUPI1_D1 PG10 UUPI1_D2 UUPI1_D2 UUPI1_D2 PG16 UUPI1_D2 PG14 UUPI1_NXT PG2 UUPI1_CLK PC2 UUPI1_CLK PC3 UUPI1_CLK PH1 RMII_0_TXD[1] PH0 RMII_0_TXD[1] PH2 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA61 RMII_0_RXD[1] PA61 RMII_0_RXD[1] PA61 RMII_0_RXD[1] PA61 RMII_0_RXD[1] PA61 RMII_0_RXD[1] PA61 <				ULPI1_D5	PG11
Nemet VLPI1_D3 PE15 ULPI1_D2 PE14 ULPI1_D0 PG15 ULPI1_NXT P15 ULPI1_LIR PC3 ULPI1_LIR PC2 EHERNET_O PH1 RMII_0_RXD[1] PH0 RMII_0_RXD[1] PH2 RMII_0_RXD[1] PA8 RMII_0_RXDV PF15 RMII_0_RXDV PF15 RMII_0_RXDV PF15 RMII_0_RXDV PF15 RMII_0_RXDV PF15 RMII_0_MDO PF14 RMII_0_RXDV PF15 RMII_0_RXDV PF15 RMII_0_MDO PF14				ULPI1_D4	PG10
WART PE14 ULPI1_D2 PE14 ULPI1_D1 PG15 ULPI1_D1 PG14 ULPI1_DR PC3 ULPI1_DR PC3 ULPI1_DR PC3 ULPI1_DR PC3 ULPI1_DR PC3 ULPI1_DR PC4 NTT PI5 ULPI1_DR PC3 ULPI1_DR PC4 NTT PI5 ULPI1_DR PC3 ULPI1_DR PC4 NTT PD5 ULPI1_DR PC3 ULPI1_DR PC4 NTT PD5 ULPI1_DR PC3 ULPI1_DR PC4 NTT PD6 RMII_0_TXD[0] PH1 RMII_0_TXD[1] PH3 RMII_0_RXD[1] PA8 RMII_0_RX_DV PF15 RMII_0_MDO PF14 RMII_0_MDO PF14 RMII_0_MDO PF14 RMII_0_MD10 PF14				ULPI1_D3	PE15
hernet				ULPI1_D2	PE14
hernet Image: Description of the second s		SW2_PH13		ULPI1_D1	PG15
hernet ULP1_STP PI4 ULP1_NXT PI5 ULP1_LIK PC2 ULP1_CLK PC2 ETHERNET_0 PH1 RMII_0_TXD[0] PH1 RMII_0_TXD[1] PH0 RMII_0_RXD[0] PA9 RMII_0_RXD[0] PA9 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA11 RMII_0_RXD[0] PA11 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA10 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA10 RMII_0_RXD[1] PA10 RMII_0_RXD[1] PA10 RMII_0_RXD[2] PG1		RIIA FLEXRAY		ULPI1_D0	PG14
hernet Image: Definition of the second se				ULPI1_STP	PI4
nemet USB ULP11_DIR PC3 ULP11_CLK PC2 ETHERNET_0 PH1 RMII_0_TXD[0] PH1 RMII_0_TXD[1] PH0 RMII_0_TXD[0] PH2 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RXDV PF11 RMII_0_MDC PG0 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1				ULPI1_NXT	PI5
Image: Description of the second s				ULPI1_DIR	PC3
Define USB USB ETHERNET_0 USB ETHERNET_0 USB POSCRIPTION POSCRIPTION PH1 RMII_0_TXD[0] PH2 RMII_0_RXD[0] PA9 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RX_DV PF15 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1				ULPI1_CLK	PC2
DESCRIPTION PORT RMII_0_TXD[0] PH1 RMII_0_TXD[1] PH0 RMII_0_TX_EN PH2 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RX_ER PA11 RMII_0_RX_DV PF15 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1	Ethernet			ETHERNET_0	
RMII_0_TXD[0] PH1 RMII_0_TXD[1] PH0 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA8 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA11 RMII_0_RXD[1] PA6 RMII_0_RXD[1] PA8 RMII_0_RX_ER PA11 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1		RV1		DESCRIPTION	PORT
RMII_0_TXD[1] PH0 RMII_0_TX_EN PH2 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RX_ER PA11 RMII_0_RX_DV PF15 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1				RMII_0_TXD[0]	PH1
RMII_0_TX_EN PH2 RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RX_ER PA11 RMII_0_RX_DV PF15 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1		D POT		RMII_0_TXD[1]	PHO
RMII_0_RXD[0] PA9 RMII_0_RXD[1] PA8 RMII_0_RX_ER PA11 RMII_0_RX_DV PF15 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1		SW1 PG		RMII_0_TX_EN	PH2
RMII_0_RXD[1] PA8 RMII_0_RX_ER PA11 RMII_0_RX_DV PF15 RMII_0_MDC PG0 RMII_0_MDIO PF14 RMII_0_TX_CLK (Reference clock) PG1				RMII_0_RXD[0]	PA9
RMII_0_RX_ERPA11RMII_0_RX_DVPF15RMII_0_MDCPG0RMII_0_MDIOPF14RMII_0_TX_CLK (Reference clock)PG1				RMII_0_RXD[1]	PA8
RMII_0_RX_DVPF15RMII_0_MDCPG0RMII_0_MDIOPF14RMII_0_TX_CLK (Reference clock)PG1				RMII_0_RX_ER	PA11
RMII_0_MDCPG0RMII_0_MDIOPF14RMII_0_TX_CLK (Reference clock)PG1				RMII_0_RX_DV	PF15
RMII_0_MDIOPF14RMII_0_TX_CLK (Reference clock)PG1				RMII_0_MDC	PG0
RMII_0_TX_CLK (Reference clock) PG1				RMII_0_MDIO	PF14
				RMII_0_TX_CLK (Reference clock)	PG1



DEVKIT-MPC5748G Board : User Peripherals





DEVKIT-MPC5748G Board : Programing Interface



JTAG	
DESCRIPTION	PIN
Support for USB Multilink	D/
Interface	F4

OpenSDA Interface						
DESCRIPTION	PI	N				
OpenSDA MCU Boot Entry	SW3					
OpenSDA micro USB:						
On-board JTAG connection via						
open source	11					
OSBDM circuit using the	JT					
MC9S08JM60						
Microcontroller						
OpenSDA JTAG:						
JTAG to update firmware in	J6					
OpenSDA MCU						



Software Development Tools

- IDE & Compilers
 - Free S32 Design Studio IDE for Power Architecture with GCC compiler
 - -GHS MULTI Integrated Development Environment
 - Cosmic IDE
 - iSystems winIDEA IDE
 - Sourcery[™] CodeBench Development Tools
- Debuggers
 - Free OpenSDA debugger on board and supported by S32DS IDE
 - P&E USB Multilink
 - -iSystems iC6000
 - -Lauterbach TRACE32 JTAG Debugger











Pre-Compiled Code Examples

- Pre-compiled example projects are available in S32DS as well as on nxp.com/DEVKIT-MPC5748G for quick start
- Example projects also includes the projects from Application Note, AN4830: Qorivva Recipes for MPC574xG

List of code examples:

- 1. Hello
- 2. Hello+pll
- 3. Hello+pll+interrupts
- 4. eDMA+ PBridge
- 5. Semaphores
- 6. Register Protection
- 7. Low Power: STOP mode
- 8. Analog-to-digital Converter
- 9. Timed I/O (eMIOS)
- 10. CAN

- 11. CAN+DMA
- 12. LIN
- 13. UART
- 14. SPI
- 15. SPI+DMA
- 16. **I**2C
- 17. Ethernet
- 18. Body Cross Trigger Unit (BCTU)
- 19. System Memory Protection Unit (SMPU)
- 20. Flash



Documentation

General Documents

- MPC5748G Microcontroller Data Sheet
- MPC5748G Microcontroller Reference Manual
- MPC5748G Microcontroller Fact Sheet
- DEVKIT-MPC5748G Board Fact Sheet
- Software Integration Guide (SWIG)

Application Notes

- AN4830: Qorivva Recipes for MPC574xG
- AN5220: MPC5748G Hardware Design Guidelines
- AN5114: Migrating between MPC5748G and MPC5746C
- AN4868: EEPROM Emulation with NXP MPC55xx, MPC56xx, and MPC57xx Microcontrollers
- AN4805: A Practical Approach to Hardware Semaphores



MPC574xG/C/B/D Family : Phantom Feature Differences

	Package				
Flash/RAM	100MAPBGA (11x11mm, 1mm)	176LQFP-EP (24x24mm, 0.5mm)	256MAPBGA (17x17mm, 1mm)	324MAPBGA (19x19mm, 1mm)	
6M/768k 6M/768k		SPC5748G SPC5748C	SPC5748G SPC5748C	SPC5748G SPC5748C	
4M/768k 4M/512k		SPC5747G SPC5747C	SPC5747G SPC5747C	SPC5747G SPC5747C	
3M/768k		SPC5746G	SPC5746G	SPC5746G	
3M/384k (512k optional) 3M/384k (512k optional)	SPC5746C SPC5746B	SPC5746C SPC5746B	SPC5746C SPC5746B	PPC5746C	Trij (op Du
2M/256k 2M/256k	SPC5745C SPC5745B	SPC5745C SPC5745B	SPC5745C SPC5745B		(all Eth <mark>Sir</mark>
1.5M/192k 1.5M/192k	SPC5744C SPC5744B	SPC5744C SPC5744B	SPC5744C SPC5744B		HS De





OpenSDA

- OpenSDA is an open-standard serial and debug adapter
- It bridges serial and debug communications between a USB host and an embedded target processor
- DEVKIT-MPC5748G comes with the OpenSDA Application preinstalled
- Follow these instructions to run the OpenSDA Bootloader and update or change the installed OpenSDA Application

Enter OpenSDA Bootloader Mode

- 1. Unplug the OpenSDA USB cable if attached
- 2. Press and hold the Reset button (SW3)
- Plug in a USB cable between a USB host and the OpenSDA USB connector (labeled "SDA")
- 4. Release the Reset button

A removable drive should now be visible in the host file system with a volume label of BOOTLOADER. You are now in OpenSDA Bootloader mode.

IMPORTANT NOTE: Follow the "Load an OpenSDA Application" instructions to update the application on your MC9S08JM60 to the latest version. It is likely that the version provided in this package is newer than what was preprogrammed on your MC9S08JM60.

Load an OpenSDA Application

- While in OpenSDA Bootloader mode, double-click SDA_INFO.HTML in the BOOTLOADER drive. A web browser will open the OpenSDA homepage containing the name and version of the installed Application. This information can also be read as text directly from SDA_INFO.HTML
- 2. Locate the **OpenSDA Applications** folder
- 3. Copy & paste or drag & drop the Application to the **BOOTLOADER** drive
- 4. Unplug the USB cable and plug it in again. The new OpenSDA Application should now be running and check the latest version by repeating Step-1

Use the same procedure to load other OpenSDA Applications.



OpenSDA

Using the Virtual Serial Port

- 1. Determine the symbolic name assigned to the DEVKIT-MPC5748G virtual serial port. On Windows platform open Device Manager and look for the COM port named "OpenSDA-CDC Serial Port".
- 2. Open the serial terminal emulation program of your choice. Examples for Windows platform include Tera Term, PuTTY, or HyperTerminal.
- 3. Program one of the "code examples" using S32 Design Studio IDE.
- 4. Configure the terminal emulation program. Most embedded examples use 8 data bits, no parity bits, and one stop bit (8-N-1). Match the baud rate to the selected serial test application and open the port.
- 5. Press and release the Reset button (SW1) at anytime to restart the example application. Resetting the embedded application will not affect the connection of the virtual serial port to the terminal program.

NOTE: Refer to the OpenSDA User's Guide for a description of a known Windows issue when disconnecting a virtual serial port while the COM port is in use.



Recommendations

- For high power/current consuming applications (like using external shield boards) use "External 12 V Supply" only.
- External 12 V Supply Specifications
 - Fully regulated Switching Power Supply
 - Input Voltage: 100-240V AC 50/60Hz
 - Output: 12V 1A/2A DC
 - Plug size: 5.5mm x 2.1 mm, Center Positive O-O
- By default "New Project" in S32 Design Studio IDE makes application to run at 16 MHz Internal RC (IRC) oscillator. For faster performance, configure PLL to desired frequency and switch clock source to PLL before executing application code.
- For faster debugging, debug from RAM, because this cuts down the lengthy Flash erase operation cycles. Follow the Software Integration Guide (SWIG) for details.
- Keep S32 Design Studio IDE and OpenSDA firmware Up-to-date for best results.
- Post Technical Questions on NXP community for <u>MPC5xxx</u>.
- Useful Links:
 - nxp.com/mpc5748g
 - nxp.com/devkit-mpc5748g
 - nxp.com/s32ds
 - nxp.com/community





SECURE CONNECTIONS FOR A SMARTER WORLD