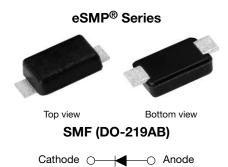
V1FM15

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Vishay General Semiconductor

# Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifiers



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.0 A			
V <sub>RRM</sub>	150 V			
I <sub>FSM</sub>	30 A			
$V_F$ at $I_F$ = 1 A ( $T_A$ = 125 °C)	0.64 V			
T <sub>J</sub> max.	175 °C			
Package	SMF (DO-219AB)			
Circuit configuration	Single			

## FEATURES

- Trench MOS Schottky technology
- Low profile package
- Ideal for automated placement
- Low forward voltage drop, low power losses
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified available
  Automotive ordering code: base P/NHM3
- Compatible to SOD-123W package case outline
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

### **MECHANICAL DATA**

Case: SMF (DO-219AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V1FM15	UNIT	
Device marking code		1MC		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	150	V	
Maximum average forward rectified current (fig.1)	I <sub>F(AV)</sub> <sup>(1)</sup>	1.0	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	30	A	
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	-40 to +175	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +175		

#### Notes

<sup>(1)</sup> Free air, mounted on FR4 PCB, 2 oz. standard footprint

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 





COMPLIANT

HALOGEN

FREE

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 0.5 A	T <sub>A</sub> = 25 °C		0.77	-	V
	I <sub>F</sub> = 1.0 A		V <sub>F</sub> <sup>(1)</sup>	1.10	1.22	
	I <sub>F</sub> = 0.5 A	- T <sub>A</sub> = 125 °C	VF ()	0.57	-	
	I <sub>F</sub> = 1.0 A			0.64	0.72	
Reverse current	V <sub>B</sub> = 100 V	T <sub>A</sub> = 25 °C		0.20	-	
	$v_{\rm R} = 100 v$ $T_{\rm A}$	T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	180	-	
	V <sub>R</sub> = 150 V	T <sub>A</sub> = 25 °C	IR (-/	-	50	μA
		T <sub>A</sub> = 125 °C		350	1500	
Typical junction capacitance	4.0 V, 1 MHz		CJ	60	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °c unless otherwise noted)				
PARAMETER	SYMBOL	V1FM15	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	125	°C/W	
	R <sub>0JM</sub> <sup>(2)</sup>	30		

#### Notes

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Device mounted on FR4 PCB, 2 oz. standard footprint, thermal resistance  $R_{\theta JA}$  – junction-to-ambient; thermal resistance  $R_{\theta JM}$  – junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V1FM15-M3/H	0.015	Н	3000	7" diameter plastic tape and reel	
V1FM15-M3/I	0.015	l	10 000	13" diameter plastic tape and reel	
V1FM15HM3/H <sup>(1)</sup>	0.015	Н	3000	7" diameter plastic tape and reel	
V1FM15HM3/I <sup>(1)</sup>	0.015	l	10 000	13" diameter plastic tape and reel	

#### Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

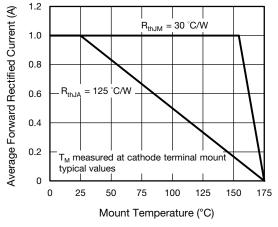


Fig. 1 - Maximum Forward Current Derating Curve

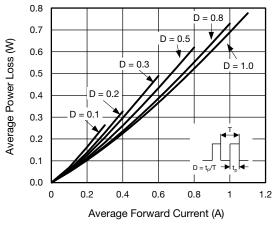


Fig. 2 - Average Power Loss Characteristics

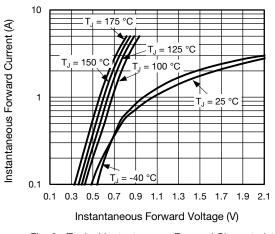


Fig. 3 - Typical Instantaneous Forward Characteristics

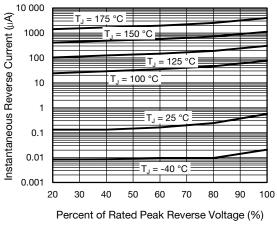
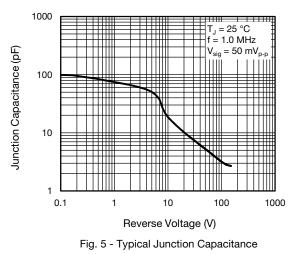


Fig. 4 - Typical Reverse Leakage Characteristics



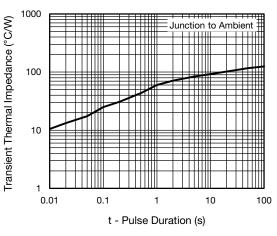


Fig. 6 - Typical Transient Thermal Impedance

Revision: 13-May-2020

3

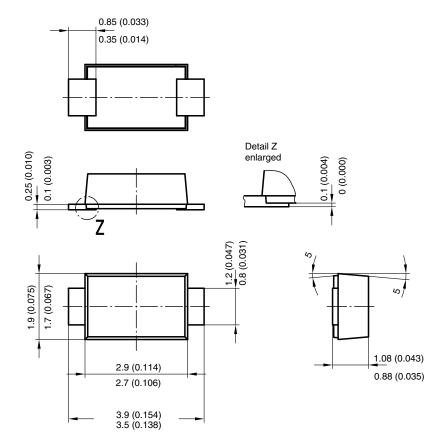
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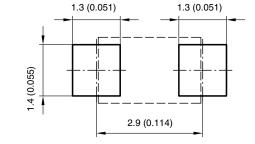


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### **PACKAGE OUTLINE DIMENSIONS** in millimeters (inches)



Foot print recommendation:



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