V3F6

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# Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifiers



## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	3.0 A		
V <sub>RRM</sub>	60 V		
I <sub>FSM</sub>	60 A		
$V_F$ at $I_F$ = 3 A ( $T_A$ = 125 °C)	0.49 V		
T <sub>J</sub> max.	150 °C		
Package	SMF (DO-219AB)		
Circuit configuration	Single		

## FEATURES

- Trench MOS Schottky technology
- Low profile package
- Ideal for automated placement
- Low forward voltage drop, low power losses
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified available
  Automotive ordering code: base P/NHM3
- Compatible to SOD-123W package case outline
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

### **MECHANICAL DATA**

Case: SMF (DO-219AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V3F6	UNIT	
Device marking code		V36		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum average forward rectified current (fig.1)	I <sub>F(AV)</sub> <sup>(1)</sup>	2.5	^	
	I <sub>F(AV)</sub> <sup>(2)</sup>	3.0	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60	А	
Operating junction temperature range	T <sub>J</sub> <sup>(3)</sup>	-40 to +150	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +150		

#### Notes

<sup>(1)</sup> Free air, mounted on FR4 PCB, 2 oz. standard footprint

<sup>(2)</sup> Mounted on FR4 PCB, 2 oz. 10 mm x 10 mm copper pad areas

 $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub>

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 1.5 A	– T <sub>A</sub> = 25 °C		0.48	-	- V
	I <sub>F</sub> = 3.0 A		V <sub>F</sub> (1)	0.54	0.62	
	I <sub>F</sub> = 1.5 A	- T <sub>A</sub> = 125 °C	VF ()	0.38	-	
	I <sub>F</sub> = 3.0 A			0.49	0.57	
Reverse current	V <sub>B</sub> = 60 V	T <sub>A</sub> = 25 °C	– I <sub>R</sub> <sup>(2)</sup> –	-	0.60	- mA
	$v_{\rm R} = 00 v$	T <sub>A</sub> = 125 °C		3	15	
Typical junction capacitance	4.0 V, 1 MHz		CJ	310	-	pF

Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °c unless otherwise noted)				
PARAMETER	SYMBOL	V3F6	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	125	°C/W	
	R <sub>θJM</sub> <sup>(3)</sup>	18	C/W	

#### Notes

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Device mounted on FR4 PCB, 2 oz. standard footprint, thermal resistance R<sub>0JA</sub> – junction-to-ambient

 $^{(3)}$  Device mounted on 10 mm x 10 mm pad size area footprint; thermal resistance  $R_{\theta JM}$  – junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V3F6-M3/H	0.015	Н	3000	7" diameter plastic tape and reel	
V3F6-M3/I	0.015	I	10 000	13" diameter plastic tape and reel	
V3F6HM3/H <sup>(1)</sup>	0.015	н	3000	7" diameter plastic tape and reel	
V3F6HM3/I <sup>(1)</sup>	0.015	I	10 000	13" diameter plastic tape and reel	

Note

<sup>(1)</sup> AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

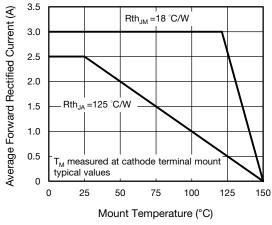


Fig. 1 - Maximum Forward Current Derating Curve

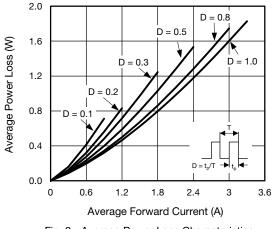
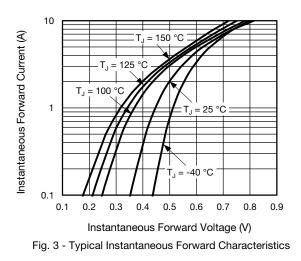


Fig. 2 - Average Power Loss Characteristics



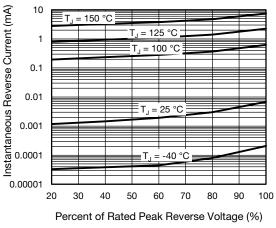
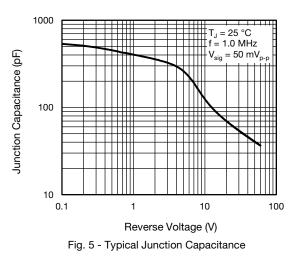


Fig. 4 - Typical Reverse Leakage Characteristics



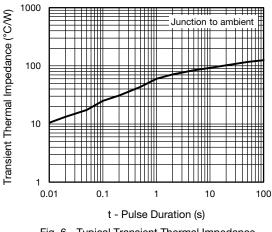


Fig. 6 - Typical Transient Thermal Impedance

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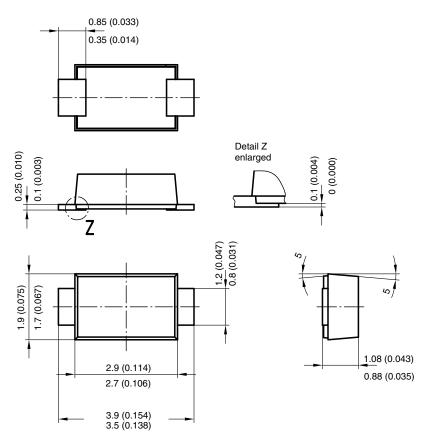
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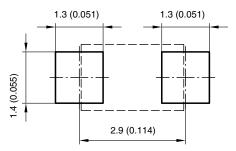
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## PACKAGE OUTLINE DIMENSIONS in millimeters (inches)



Foot print recommendation:



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