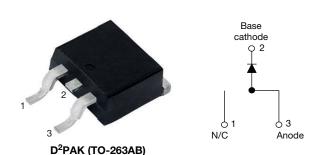


www.vishay.com

Vishay Semiconductors

High Performance Schottky Rectifier, 10 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	10 A						
V_{R}	35 V, 45 V						
V _F at I _F	0.49 V						
I _{RM} max.	15 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	13 mJ						
Package	D ² PAK (TO-263AB)						
Circuit configuration	Single						

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy FREE encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-10TQ...S-M3 Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES UNITS									
I _{F(AV)}	Rectangular waveform	10	Α						
V _{RRM}		35/45	V						
I _{FSM}	t _p = 5 μs sine	1050	Α						
V _F	10 A _{pk} , T _J = 125 °C	0.49	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-10TQ035S-M3	VS-10TQ045S-M3	UNITS				
Maximum DC reverse voltage	V_R	35	45	V				
Maximum working peak reverse voltage	V_{RWM}	33	45	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 151 °C	10	Α				
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	1050	Α			
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	280				
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6.5 mH		13	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum V_A = 1.5 x V_R typical		2	А			

www.vishay.com

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		10 A	T _{.1} = 25 °C	0.57				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	20 A	1j = 25 C	0.67	v			
See fig. 1	V _{FM} (1)	10 A	T.ı = 125 °C	0.49	V			
		20 A	- IJ = 125 C	0.61				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	2	mA			
See fig. 2		T _J = 125 °C	v _R = nateu v _R	15				
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz), 25 °C		900	pF			
Typical series inductance	L _S	Measured lead to lead 5	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C		
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50			
Approximate weight				2	g		
Approximate weight				0.07	oz.		
Mauratic a taurus minimum				6 (5)	kgf · cm		
Mounting torque	maximum			12 (10)	(lbf · in)		
Marking device			Case style D ² PAK (TO-263AB)	10TQ035S 10TQ045S			

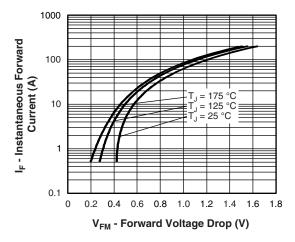


Fig. 1 - Maximum Forward Voltage Drop Characteristics

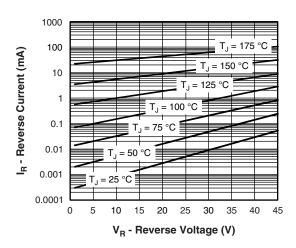


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

Vishay Semiconductors

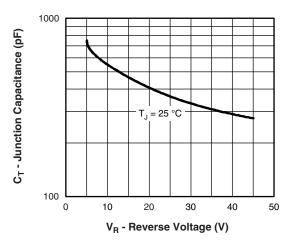


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

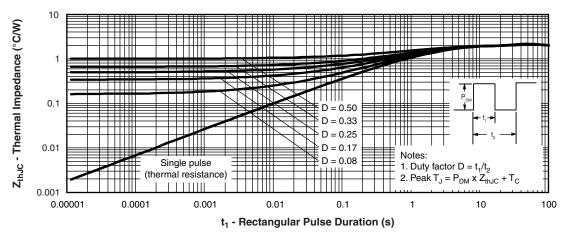


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

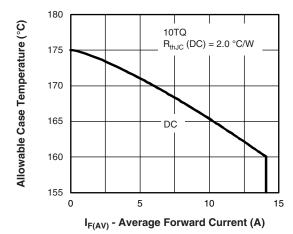


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

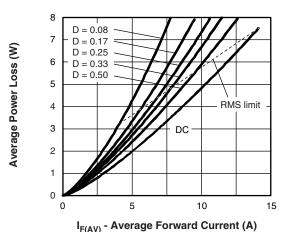


Fig. 6 - Forward Power Loss Characteristics

Vishay Semiconductors

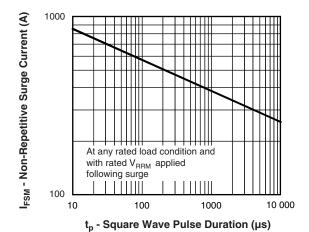


Fig. 7 - Maximum Non-Repetitive Surge Current

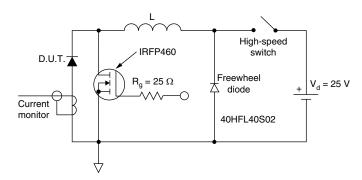
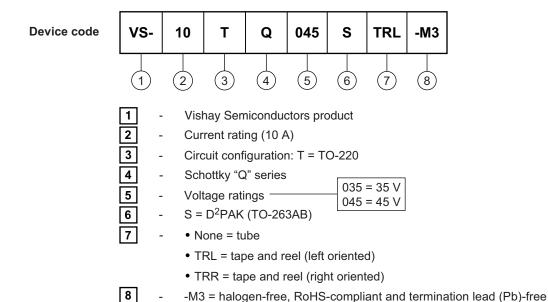


Fig. 8 - Unclamped Inductive Test Circuit

ORDERING INFORMATION TABLE





VS-10TQ035S-M3, VS-10TQ045S-M3

Vishay Semiconductors

ORDERING INFORMATION								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-10TQ035S-M3	50	Antistatic plastic tubes						
VS-10TQ035STRR-M3	800	13" diameter plastic tape and reel						
VS-10TQ035STRL-M3	800	13" diameter plastic tape and reel						
VS-10TQ045S-M3	50	Antistatic plastic tubes						
VS-10TQ045STRR-M3	800	13" diameter plastic tape and reel						
VS-10TQ045STBL-M3	800	13" diameter plastic tape and reel						

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96164				
Part marking information	www.vishay.com/doc?95444				
Packaging information	www.vishay.com/doc?96424				



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.