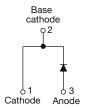


www.vishay.com

Vishay Semiconductors

Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 15 A FRED Pt[®]





PRIMARY CHARACTERISTICS								
I _{F(AV)}	15 A							
V_R	600 V							
V _F at I _F	0.85 V							
t _{rr} typ.	60 ns							
T _J max.	175 °C							
Package	TO-220AC 2L							
Circuit configuration	Single							

FEATURES

- · Hyperfast recovery time
- Benchmark ultralow forward voltage drop
- 175 °C operating junction temperature
- Low leakage current



- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Peak repetitive reverse voltage	V_{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 154 °C	15							
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	250	Α						
Peak repetitive forward current	I _{FM}		30							
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS									
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	.,				
Farmand walks as	V _F	I _F = 15 A	-	0.99	1.05	V				
Forward voltage		I _F = 15 A, T _J = 150 °C	-	0.85	0.92					
Reverse leakage current		$V_R = V_R$ rated	-	0.1	10	μA				
neverse leakage current	I _R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	15	120	μΑ				
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF				
Series inductance L _S Measured lead to lead 5 mm from package bo		-	8.0	-	nH					



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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
	t _{rr}	$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A}$	-	60	120					
Poverse receivery time		$I_F = 15 \text{ A}, dI_F/dt = 100$	-	190	270					
Reverse recovery time		T _J = 25 °C		-	220	-	ns - A - μC			
		T _J = 125 °C	I _F = 15 A dI _F /dt = 200 A/µs	-	320	-				
Dook room ourrent	I _{RRM}	T _J = 25 °C		-	19	-				
Peak recovery current		T _J = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	26	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C	VK = 290 V	-	2.2	-				
		T _J = 125 °C		-	4.3	-				

THERMAL MECHANICAL SPECIFICATIONS									
PARAMETER	MIN.	TYP.	MAX.	UNITS					
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C			
Thermal resistance, junction-to-case	R _{thJC}		-	1.0	1.3				
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W			
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-				
Weight			-	2.0	-	g			
weight			-	0.07		oz.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-220AC 2L	15ETL06						

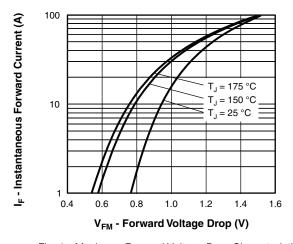


Fig. 1 - Maximum Forward Voltage Drop Characteristics

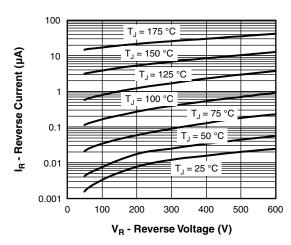


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

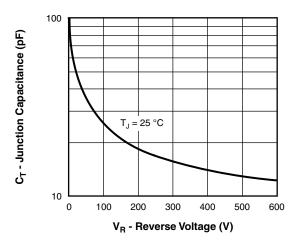


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

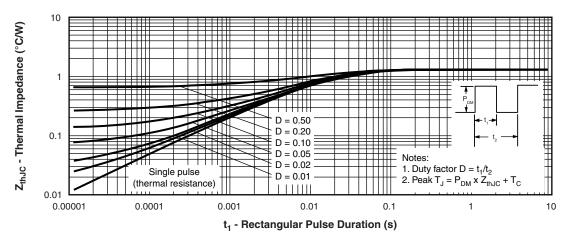


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

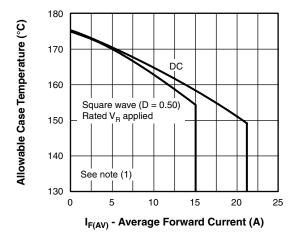


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

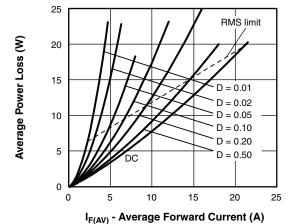


Fig. 6 - Forward Power Loss Characteristics

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R



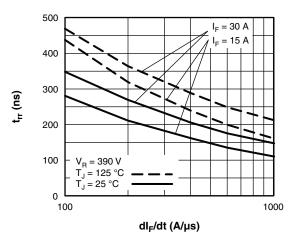


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

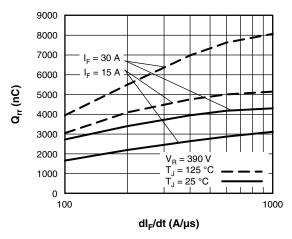
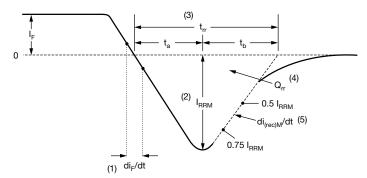


Fig. 8 - Typical Stored Charge vs. dl_E/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

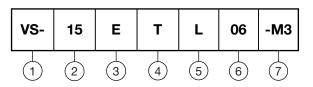
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- **2** Current rating (15 = 15 A)
- **3** E = single diode
- T = TO-220, D²PAK (TO-263AB)
- 5 L = ultralow V_F hyperfast recovery
- 6 Voltage rating (06 = 600 V)
- 7 Environmental digit:
 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

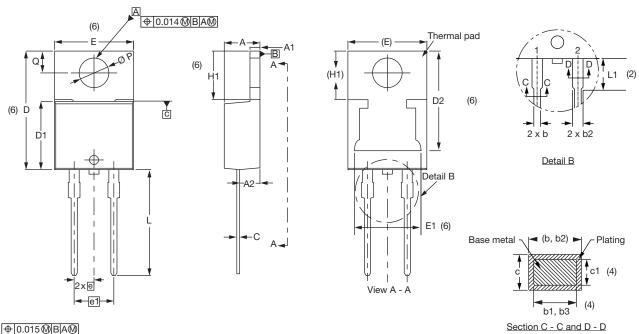
ORDERING INFORMATION (Example)									
PREFERRED P/N	ERRED P/N BASE QUANTITY PACKAGING DESCRIPTION								
VS-15ETL06-M3	50	Antistatic plastic tubes							

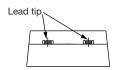
LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?96156							
Part marking information	www.vishay.com/doc?95391							
SPICE model	www.vishay.com/doc?96051							



2L TO-220AC

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIM	IETERS	INC	INCHES		NOTES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIBOL	MIN.		MAX.	MIN.	MAX.	NOTES		
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7		
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6		
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6		
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105			
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208			
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6		
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552			
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2		
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154			
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118			
D1	8.38	9.02	0.330	0.355				•	•					

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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