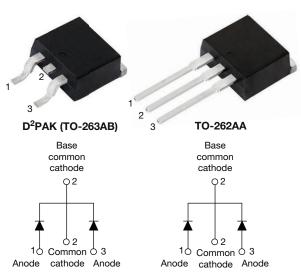
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Vishay Semiconductors

# High Performance Schottky Rectifier, 2 x 15 A



VS-25CTQ...S-M3

VS-25CTQ...-1-M3

PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub>	2 x 15 A						
V <sub>R</sub>	35 V, 40 V, 45 V						
V <sub>F</sub> at I <sub>F</sub>	0.50 V						
I <sub>RM</sub> typ.	70 mA at 125 °C						
T <sub>J</sub> max.	150 °C						
E <sub>AS</sub>	20 mJ						
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA						
Circuit configuration	Common cathode						

### FEATURES

- 150 °C T<sub>J</sub> operation
- · Center tap configuration
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245  $^{\circ}\mathrm{C}$
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-25CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	VALUES	UNITS							
I <sub>F(AV)</sub>	Rectangular waveform	30	A						
V <sub>RRM</sub>	Range	35 to 45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	990	A						
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.50	V						
TJ	Range	-55 to +150	°C						

VOLTAGE RATINGS							
PARAMETER         SYMBOL         VS-25CTQ035S-M3 VS-25CTQ035-1-M3         VS-25CTQ040S-M3 VS-25CTQ040-1-M3         VS-25CTQ045S-M3 VS-25CTQ045-1-M3         UNITS							
Maximum DC reverse voltage	V <sub>R</sub>	35	40	45	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>		40	40	v		

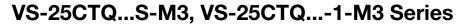
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Document Number: 94930

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_{C}$ = 102 °C	30					
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	990	А			
non-repetitive surge current per leg See fig. 7			load condition and with rated V <sub>RRM</sub> applied	250				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 3 \ A, \ L = 4.40$	20	mJ				
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by $T_J$ maxim	3	А				

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
Maximum forward voltage drop per leg See fig. 1		15 A	T <sub>1</sub> = 25 °C	0.56			
	V <sub>FM</sub> <sup>(1)</sup>	30 A	1j=25 C	0.71	V		
	VFM ()	15 A	T <sub>1</sub> = 125 °C	0.50			
		30 A	1j=125 C	0.64			
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1.75	mA		
per leg		T <sub>J</sub> = 125 °C	VR - Haleu VR	110			
Typical reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	70	mA		
Maximum junction capacitance per leg	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		900	pF		
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mr	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs			

### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C				
Maximum thermal resistance, junction to case per leg	P	DC operation See fig. 4	3.25					
Maximum thermal resistance, junction to case per package	– R <sub>thJC</sub>	DC operation	1.63	°C/W				
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50					
Approximate weight			2	g				
Approximate weight			0.07	oz.				
Mounting torque	num		6 (5)	kgf · cm				
Mounting torque maxin	num		12 (10)	(lbf · in)				
		Case style D <sup>2</sup> PAK (TO-263AB)	25CTQ 25CTQ 25CTQ	040S				
Marking device		Case style TO-262AA	25CTQ0 25CTQ0 25CTQ0 25CTQ0	040-1				

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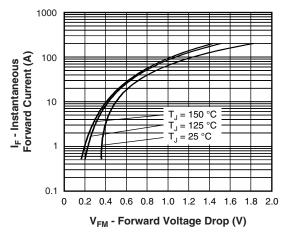
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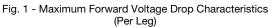
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# VS-25CTQ...S-M3, VS-25CTQ...-1-M3 Series

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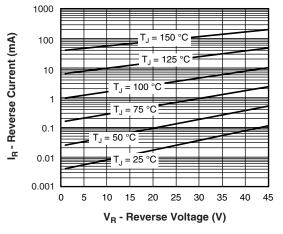


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

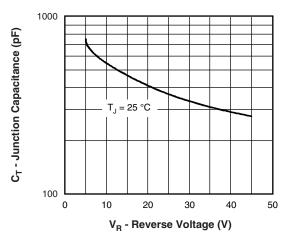


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

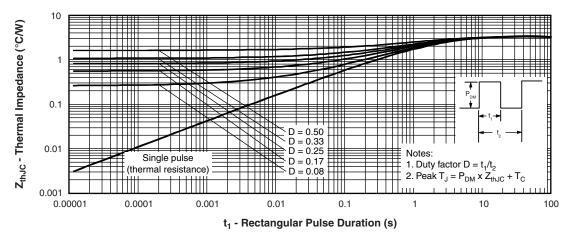


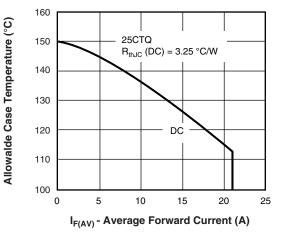
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

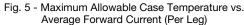
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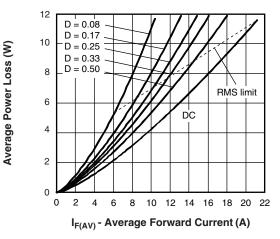


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

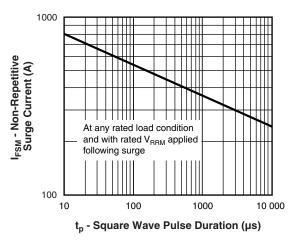


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

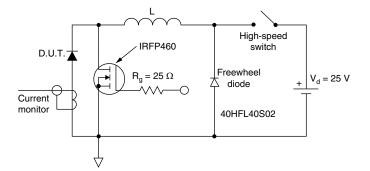


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>BEV</sub> = inverse power loss =  $V_{B1} \times I_B (1 - D)$ ;  $I_B$  at  $V_{B1} = 80$  % rated  $V_B$

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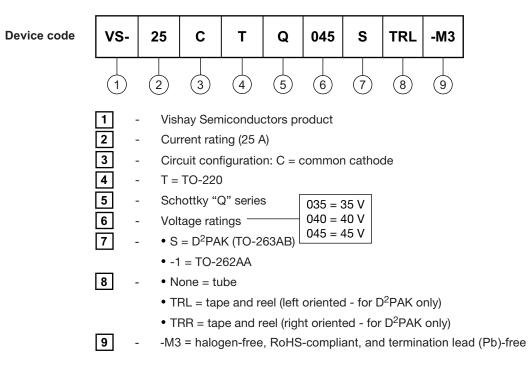
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# VS-25CTQ...S-M3, VS-25CTQ...-1-M3 Series

## **Vishay Semiconductors**

## **ORDERING INFORMATION TABLE**



ORDERING INFORMATION								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-25CTQ035S-M3	50	Antistatic plastic tubes						
VS-25CTQ035STRL-M3	800	13" diameter plastic tape and reel						
VS-25CTQ035STRR-M3	800	13" diameter plastic tape and reel						
VS-25CTQ040S-M3	50	Antistatic plastic tubes						
VS-25CTQ040STRL-M3	800	13" diameter plastic tape and reel						
VS-25CTQ040STRR-M3	800	13" diameter plastic tape and reel						
VS-25CTQ045S-M3	50	Antistatic plastic tubes						
VS-25CTQ045STRL-M3	800	13" diameter plastic tape and reel						
VS-25CTQ045STRR-M3	800	13" diameter plastic tape and reel						

LINKS TO RELATED DOCUMENTS								
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164						
	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444						
	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						
SPICE model		www.vishay.com/doc?95285						

# **Outline Dimensions**



D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

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SYMBOL	MILLIMETERS		INCHES		ICHES NOTES		SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

Revision: 08-Jul-15

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## **Outline Dimensions**



**Vishay Semiconductors** 

**TO-262** 

### **DIMENSIONS** in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

3. - Anode

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

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