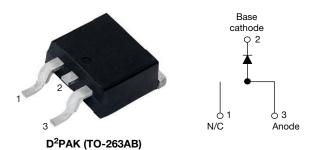
VS-18TQ035S-M3, VS-18TQ040S-M3, VS-18TQ045S-M3

**Vishay Semiconductors** 

## High Performance Schottky Rectifier, 18 A



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SHAY

PRIMARY CHARACTERISTICS									
I <sub>F(AV)</sub>	18 A								
V <sub>R</sub>	35 V, 40 V, 45 V								
V <sub>F</sub> at I <sub>F</sub>	0.53 V								
I <sub>RM</sub>	25 mA at 125 °C								
T <sub>J</sub> max.	175 °C								
E <sub>AS</sub>	24 mJ								
Package	D <sup>2</sup> PAK (TO-263AB)								
Circuit configuration	Single								

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- HALOGEN • High purity, high temperature epoxy FREE encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

The VS-18TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	18	A						
V <sub>RRM</sub>	Range	35 to 45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1800	A						
V <sub>F</sub>	18 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.53	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS									
PARAMETER SYMBOL VS-18TQ035S-M3 VS-18TQ040S-M3 VS-18TQ045S-M3 UNITS									
Maximum DC reverse voltage	um DC reverse voltage V <sub>R</sub>		40	45	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	40	40	v				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS					
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_C = 149$ °C	18	А					
Maximum peak one cycle		5 $\mu$ s sine or 3 $\mu$ s rect. pulse	Following any rated	1800					
non-repetitive surge current See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	390	A				
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 3.6 \ A, \ L = 3.7$	24	mJ					
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to ze Frequency limited by $T_J$ maxim	3.6	А					

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COMPLIANT

## VS-18TQ035S-M3, VS-18TQ040S-M3, VS-18TQ045S-M3

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### **Vishay Semiconductors**

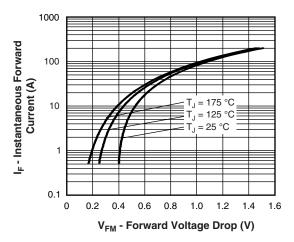
ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS							
		18 A	T.I = 25 °C	0.60						
Maximum forward voltage drop	V <sub>EM</sub> <sup>(1)</sup>	36 A	$1_{\rm J} = 25$ C	0.72	V					
See fig. 1	V FM (*)	18 A	T 105 %C	0.53	v					
		36 A	T <sub>J</sub> = 125 °C	0.67						
Maximum reverse leakage current	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	2.5	mA					
See fig. 2	IRM (''	T <sub>J</sub> = 125 °C	$v_{\rm R}$ = Raled $v_{\rm R}$	25						
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1400	pF					
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5	8.0	nH						
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs						

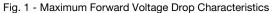
Note

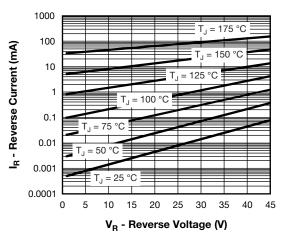
SHAY

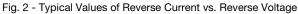
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECH	THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	PARAMETER		TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C					
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	1.50	°C/W					
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50	0/10					
Approximate weight				2	g					
Approximate weight				0.07	oz.					
Mounting torque	minimum			6 (5)	kgf · cm					
Mounting torque maximu				12 (10)	(lbf · in)					
Marking device				18TQ	035S					
			Case style D <sup>2</sup> PAK (TO-263AB)	18TQ	040S					
				18TQ	045S					

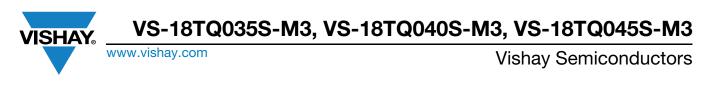








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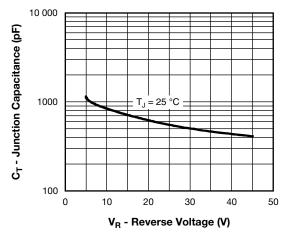


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

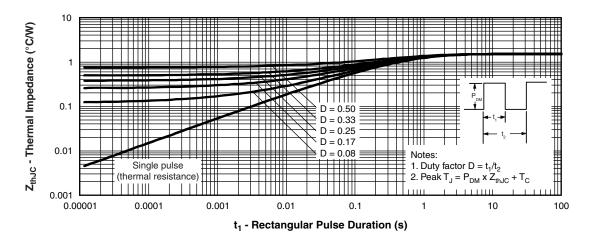


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

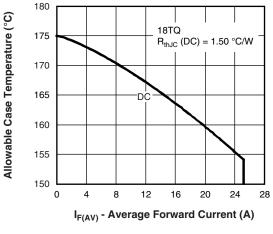


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

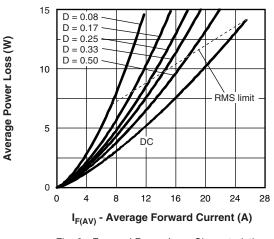


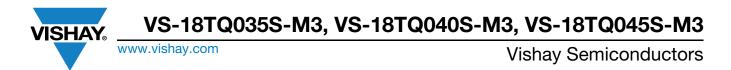
Fig. 6 - Forward Power Loss Characteristics

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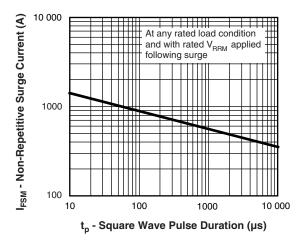


Fig. 7 - Maximum Non-Repetitive Surge Current

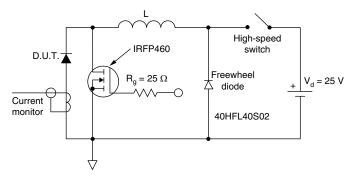


Fig. 8 - Unclamped Inductive Test Circuit

VS-18TQ035S-M3, VS-18TQ040S-M3, VS-18TQ045S-M3

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### **Vishay Semiconductors**

### **ORDERING INFORMATION TABLE**

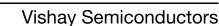
VISHAY

Device code	VS-	18	т	Q	045	S	TRL	-M3
		2	3	4	5	6	7	8
	1 -   2 -   3 -   4 -   5 -   6 -	Cur Circ Sch Volt	rrent rati cuit cont nottky "( tage rati	ing (18 A figuratio Q" series	n: T = T s	O-220	935 = 35 940 = 40 945 = 45	V
	7.		one = tu RL = tap		eel (left	oriented	d)	
	8 -				eel (righ e, RoHS		,	d termi

ORDERING INFORMATION										
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION								
VS-18TQ035S-M3	50	Antistatic plastic tubes								
VS-18TQ035STRL-M3	800	13" diameter plastic tape and reel								
VS-18TQ035STRR-M3	800	13" diameter plastic tape and reel								
VS-18TQ045S-M3	50	Antistatic plastic tubes								
VS-18TQ045STRL-M3	800	13" diameter plastic tape and reel								
VS-18TQ045STRR-M3	800	13" diameter plastic tape and reel								

LINKS TO RELATED DOCUMENTS								
Dimensions www.vishay.com/doc?96164								
Part marking information	www.vishay.com/doc?95444							
Packaging information	www.vishay.com/doc?96424							
SPICE model	www.vishay.com/doc?96209							

# **Outline Dimensions**

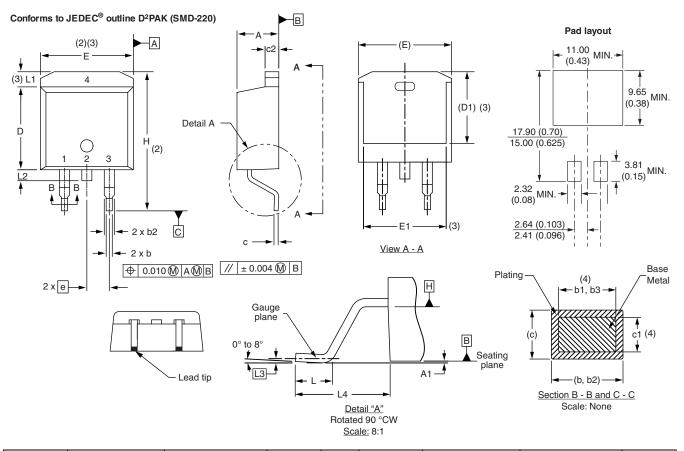


D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

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SHA



SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES		MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		e 2.54 BSC 0.100 BSC		BSC			
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

Revision: 08-Jul-15

1

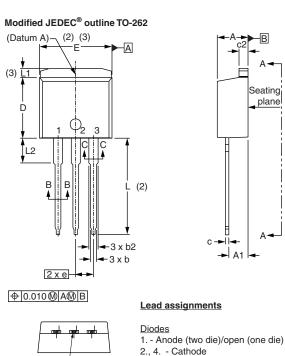
## **Outline Dimensions**



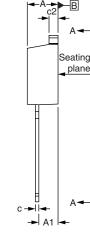
**Vishay Semiconductors** 

**TO-262** 

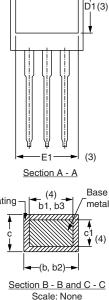
#### **DIMENSIONS** in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

3. - Anode

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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Document Number: 95419

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