



D5V0F1U2LPQ

Features

- IEC 61000-4-2 (ESD): Air ±20kV, Contact ±15kV
- 1 Channel of ESD Protection
- Low Channel Input Capacitance of 0.5pF Typical
- Low Profile Package (0.53mm Max) and Ultra-Small PCB Footprint Area (1.08mm * 0.68mm Max) Suitable for Compact Portable Electronics
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVITM, HDMITM, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The D5V0F1U2LPQ is suitable for automotive applications requiring specific change control; it is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.
- <u>https://www.diodes.com/quality/product-definitions/</u>

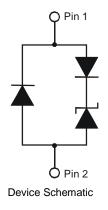
1 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Mechanical Data

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.001 grams (Approximate)



Bottom View



Ordering Information (Note 4)

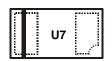
| Part Num | ber | Compliance | Marking | Reel Size (inches) | Tape Width (mm) | Quantity per Reel |
|--|-----|------------|---------|--------------------|-----------------|--------------------|
| D5V0F1U2LPQ-7B | | Automotive | U7 | 7 | 8 | 10,000/Tape & Reel |
| Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. | | | | | | |

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



U7 = Product Type Marking Code Bar Denotes Pin 1



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
|------------------------------------|--------------------------|-------|------|------------------------|
| Peak Pulse Current | IPP | 1.5 | А | 8/20µs, Per Figure 3 |
| ESD Protection – Contact Discharge | V _{ESD_CONTACT} | ±15 | kV | Standard IEC 61000-4-2 |
| ESD Protection – Air Discharge | Vesd_air | ±20 | kV | Standard IEC 61000-4-2 |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Package Power Dissipation (Note 5) | PD | 250 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{	heta JA}$ | 500 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -65 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Conditions |
|--|------------------|-----|-----|------|------|---|
| Reverse Working Voltage | V _{RWM} | _ | — | 5.5 | V | — |
| Reverse Current (Note 6) | I _R | | — | 100 | nA | $V_R = 5.5V$ |
| Reverse Breakdown Voltage | V _{BR} | 6.0 | — | — | V | I _R = 1mA |
| Reverse Clamping Voltage, Positive Transients (Note 7) | V _{CL} | _ | 10 | 12 | V | I _{PP} = 1A, t _P = 8/20μs |
| Dynamic Resistance | R _{DYN} | _ | 0.9 | — | Ω | I _R = 1A, t _P = 8/20µs |
| Capacitance (Note 8) | CT | _ | 0.4 | 0.65 | pF | V _R = 2.5V, f = 1MHz |
| | | _ | 0.5 | — | pF | $V_R = 0V$, f = 1MHz |

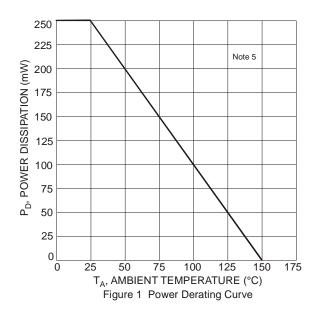
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

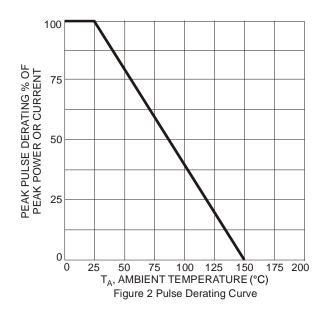
6. Short duration pulse test used to minimize self-heating effect.

7. Clamping voltage value is based on an 8x20µs peak pulse current (Ipp) waveform.

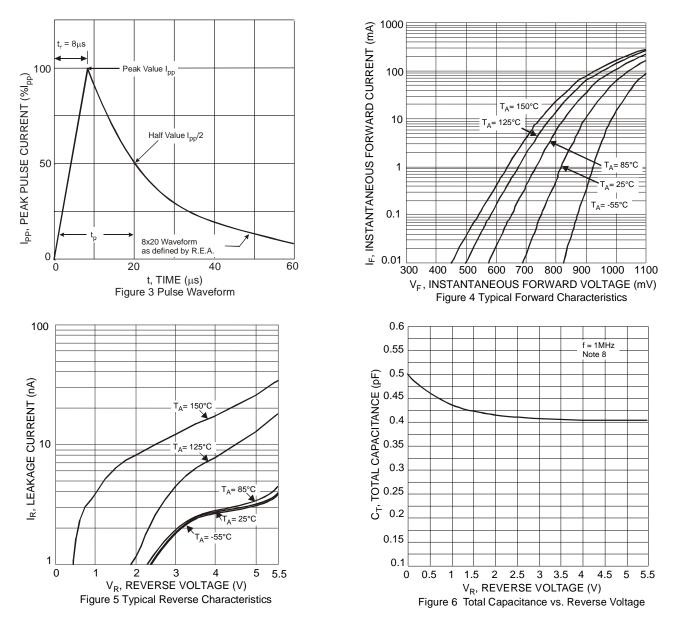
8. Measured from any I/O to GND.

9. For information on the impact of Diodes Incorporated's USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destools/appnote_dnote.html.



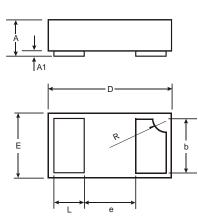






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



X1-DFN1006-2

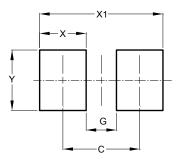
| X1-DFN1006-2 | | | | | |
|----------------------|------|-------|------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 0.47 | 0.53 | 0.50 | | |
| A1 | 0 | 0.05 | 0.03 | | |
| b | 0.45 | 0.55 | 0.50 | | |
| D | 0.95 | 1.075 | 1.00 | | |
| Е | 0.55 | 0.675 | 0.60 | | |
| е | - | - | 0.40 | | |
| L | 0.20 | 0.30 | 0.25 | | |
| R | 0.05 | 0.15 | 0.10 | | |
| All Dimensions in mm | | | | | |



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-2



| Dimensions | Value (in mm) | | | |
|------------|---------------|--|--|--|
| С | 0.70 | | | |
| G | 0.30 | | | |
| Х | 0.40 | | | |
| X1 | 1.10 | | | |
| Y | 0.70 | | | |

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