74AUP1G157-Q100

Low-power 2-input multiplexer

Rev. 2 — 4 November 2021

Product data sheet

1. General description

The 74AUP1G157-Q100 is a single 2-input multiplexer. Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V. This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 0.8 V to 3.6 V
- · CMOS low power dissipation
- High noise immunity
- · Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8C (2.7 V to 3.6 V)
- ESD protection:
 - MIL-STD-883, method 3015 Class 3A. Exceeds 5000 V
 - HBM JESD22-A114F Class 3A. Exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)

3. Ordering information

Table 1. Ordering information

Type number	Package								
	Temperature range	Name	Description	Version					
74AUP1G157GM-Q100	-40 °C to +125 °C		plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886					



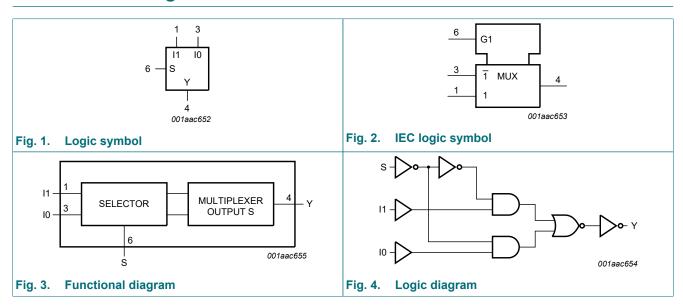
4. Marking

Table 2. Marking

Type number	Marking code [1]
74AUP1G157GM-Q100	аР

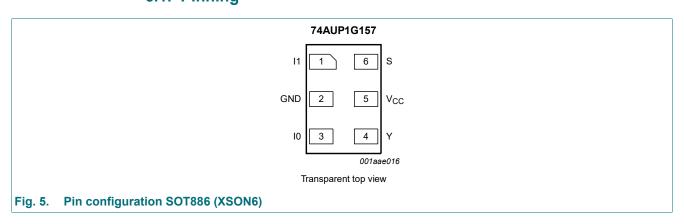
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
I1	1	data input from source 1
GND	2	ground (0 V)
10	3	data input from source 0
Υ	4	multiplexer output
V _{CC}	5	supply voltage
S	6	common data select input

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$

Input	Output		
S	11	10	Υ
L	X	L	L
L	X	Н	Н
Н	L	X	L
Н	Н	X	Н

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	V _O = 0 V to V _{CC}	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C to } +125 ^{\circ}\text{C}$ [2]	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

^[2] For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
T _{amb} = 2	5 °C	·					
V _{IH}	HIGH-level input	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V	
	voltage	V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V	
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V	
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V	
V _{IL}	LOW-level input	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V	
	voltage	V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V	
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V	
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V	
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL}					
	voltage	I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V	
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V	
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V	
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V	
			I_{O} = -2.3 mA; V_{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V	
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.72	-	-	V	
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.6	-	-	V	
V _{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL}					
	voltage	I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V	
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V	
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V	
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V	
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V	
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V	
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V	
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V	

4 / 13

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
l _l	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	μA
l _{OFF}	power-off leakage current	V_1 or $V_0 = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.2	μA
ΔI_{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μΑ
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.5	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC}$ - 0.6 V; $I_O = 0$ A; $V_{CC} = 3.3$ V; One input at V_{CC} - 0.6 V, other inputs at V_{CC} or GND.	-	-	40	μA
C _I	input capacitance	V_{CC} = 0 V to 3.6 V; V_I = GND or V_{CC}	-	8.0	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF
T _{amb} = -4	40 °C to +85 °C				1	
V _{IH}	HIGH-level input	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
	voltage	V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL}				
	voltage	I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL}				
	voltage	I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
l _l	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μA
ΔI _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μΑ
I _{CC}	supply current	$V_1 = GND \text{ or } V_{CC}; I_O = 0 \text{ A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC}$ - 0.6 V; $I_O = 0$ A; $V_{CC} = 3.3$ V; One input at V_{CC} - 0.6 V, other inputs at V_{CC} or GND.	-	-	50	μA
T _{amb} = -4	40 °C to +125 °C					
V _{IH}	HIGH-level input	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
	voltage	V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
	voltage	V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL}				
	voltage	I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.40	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL}				
	voltage	$I_O = 20 \mu A; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC}$ - 0.6 V; $I_O = 0$ A; $V_{CC} = 3.3$ V; One input at V_{CC} - 0.6 V, other inputs at V_{CC} or GND.	-	-	75	μA

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 5 p	F			•		,			'	
t _{pd}	propagation	I0, I1 or S to Y; see <u>Fig. 6</u> [2]								
	delay	V _{CC} = 0.8 V	-	19.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.3	5.7	11.2	2.1	11.4	2.1	12.6	ns
		V _{CC} = 1.4 V to 1.6 V	1.7	4.0	6.5	1.9	7.0	1.9	7.7	ns
		V _{CC} = 1.65 V to 1.95 V	1.6	3.2	5.2	1.5	5.8	1.5	6.4	ns
		V _{CC} = 2.3 V to 2.7 V	1.4	2.5	3.8	1.1	4.2	1.1	4.7	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	2.2	3.2	0.9	3.5	0.9	3.9	ns
C _L = 10	pF									
t _{pd}	propagation	I0, I1 or S to Y; see <u>Fig. 6</u> [2]								
	delay	V _{CC} = 0.8 V	-	23.5	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.7	6.6	12.8	2.4	13.0	2.4	14.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	4.6	7.5	2.3	8.1	2.3	9.0	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.8	6.0	1.8	6.7	1.8	7.4	ns
		V _{CC} = 2.3 V to 2.7 V	1.8	3.0	4.5	1.5	5.0	1.5	5.5	ns
		V _{CC} = 3.0 V to 3.6 V	1.6	2.8	3.9	1.3	4.2	1.3	4.7	ns
C _L = 15	pF									
t _{pd}	propagation	I0, I1 or S to Y; see <u>Fig. 6</u> [2]								
	delay	V _{CC} = 0.8 V	-	27.2	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.1	7.4	14.3	2.7	14.8	2.7	16.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.4	5.1	8.5	2.6	9.2	2.6	10.2	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	4.2	6.8	2.0	7.6	2.0	8.4	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.5	5.1	1.8	5.7	1.8	6.3	ns
		V _{CC} = 3.0 V to 3.6 V	1.9	3.2	4.5	1.6	4.9	1.6	5.4	ns
C _L = 30	pF			•		1			'	
t _{pd}	propagation	I0, I1 or S to Y; see <u>Fig. 6</u> [2]								
	delay	V _{CC} = 0.8 V	-	35.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.1	9.6	19.1	3.5	19.9	3.5	21.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.2	6.7	11.1	3.3	12.1	3.3	13.4	ns
		V _{CC} = 1.65 V to 1.95 V	3.0	5.5	8.9	2.6	10.1	2.6	11.2	ns
		V _{CC} = 2.3 V to 2.7 V	2.8	4.6	6.6	2.5	7.5	2.5	8.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.6	4.3	6.0	2.3	6.4	2.3	7.1	ns

Symbol	Parameter	Conditions	25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 5 p	F, 10 pF, 15 p	F and 30 pF						'		•
C _{PD}	power	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]								
	dissipation capacitance	V _{CC} = 0.8 V	-	2.6	-	-	-	-	-	pF
	Capacitarioc	V _{CC} = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	рF
		V _{CC} = 1.65 V to 1.95 V	-	2.9	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.4	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF

- [1] All typical values are measured at nominal V_{CC}.
- [2] t_{pd} is the same as t_{PLH} and t_{PHL} .
- [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

11.1. Waveforms and test circuit

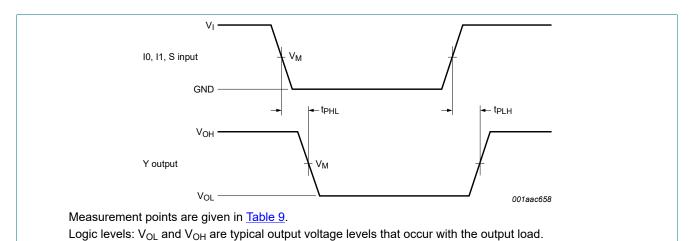
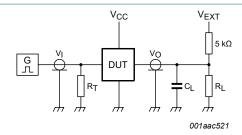


Fig. 6. The data inputs (I0, I1) and common data select input (S) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input		
V _{CC}	V _M	V _M	V _I	$t_r = t_f$
0.8 V to 3.6 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{CC}	≤ 3.0 ns



Test data is given in Table 10.

Definitions for test circuit:

 R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Fig. 7. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load	V _{EXT}			
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times R_L = 5 k Ω . For measuring propagation delays, set-up and hold times and pulse width R_L = 1 M Ω .

12. Package outline

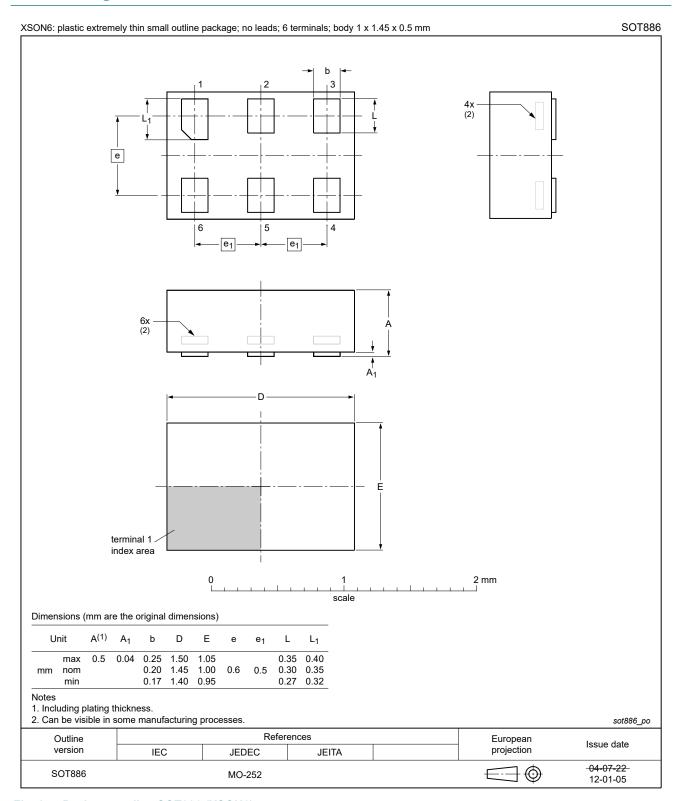


Fig. 8. Package outline SOT886 (XSON6)

10 / 13

13. Abbreviations

Table 11. Abbreviations

Acronym	Description
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MIL	Military
MM	Machine Model

14. Revision history

Table 12. Revision history

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Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G157_Q100 v.2	20211104	Product data sheet	-	74AUP1G157_Q100 v.1
Modifications:	 Section 1 and Section 2 updated. Section 8: Derating values for P_{tot} total power dissipation updated. 			
74AUP1G157_Q100 v.1	20190128	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Contents

1	General description	1
2.	Features and benefits	1
3.	Ordering information	1
4.	Marking	2
5.	Functional diagram	2
6.	Pinning information	2
6.1	. Pinning	2
6.2	Pin description	3
7.	Functional description	3
8.	Limiting values	3
9.	Recommended operating conditions	4
10.	Static characteristics	4
11.	Dynamic characteristics	7
11.	Waveforms and test circuit	8
12.	Package outline	10
13.	Abbreviations	11
14.	Revision history	11
15.	Legal information	12

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