VSMY1850ITX01



High Speed Infrared Emitting Diodes, 850 nm, Surface Emitter Technology



www.vishay.com

DESCRIPTION

As part of the <u>SurfLightTM</u> portfolio, the VSMY1850ITX01 is an infrared, 850 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, molded in clear, untinted 0805 plastic package for surface mounting (SMD).

FEATURES

- Package type: surface mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.85
- AEC-Q101 qualified
- Enhanced operating temperature range: -40 °C to +105 °C
- Peak wavelength: $\lambda_p = 850 \text{ nm}$
- High reliability
- High radiant power
- · High radiant intensity
- High speed
- Angle of half intensity: $\phi = \pm 60^{\circ}$
- Suitable for high pulse current operation
- 0805 standard surface-mountable package
- Floor life: 72 h, MSL 4, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Miniature light barrier
- Photointerrupters
- Optical switch
- Emitter source for proximity sensors
- IR touch panels
- IR flash
- IR illumination
- Emitter for automotive applications (e.g. rain sensor)

PRODUCT SUMMARY				
COMPONENT	l _e (mW/sr)	φ (deg)	λ _p (nm)	t _r (ns)
VSMY1850ITX01	10	± 60	850	10

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY1850ITX01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805	

Note

MOQ: minimum order quantity

1 For technical questions, contact: <u>emittertechsupport@vishay.com</u> 

RoHS

COMPLIANT HALOGEN

FREE

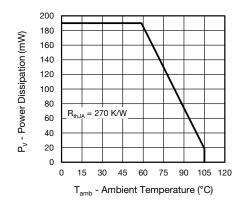
GREEN (5-2008)

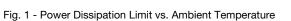
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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	100	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I _{FM}	200	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	1	A	
Power dissipation		Pv	190	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T _{amb}	-40 to +105	°C	
Storage temperature range		T _{stg}	-40 to +110	°C	
Soldering temperature	According to Fig. 7, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction / ambient	JESD 51	R _{thJA}	270	K/W	





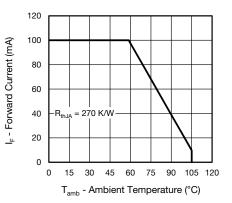


Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 100 mA, t _p = 20 ms	V _F	-	1.65	1.9	V
	$I_F = 1 \text{ A}, t_p = 100 \ \mu \text{s}$	V _F	-	2.9	-	V
Temperature coefficient of $V_{\rm F}$	I _F = 1 mA	TK _{VF}	-	-1.4	-	mV/K
	I _F = 10 mA	TK _{VF}	-	-1.18	-	mV/K
Reverse current		I _R	Not designed for reverse operation			μA
Junction capacitance	$V_{R} = 0 V, f = 1 MHz,$ E = 0 mW/cm ²	CJ	-	125	v	pF
Radiant intensity	I _F = 100 mA, t _p = 20 ms	Ι _e	5	10	15	mW/si
	$I_F = 1 \text{ A}, t_p = 100 \ \mu \text{s}$	l _e	-	85	-	mW/sr
Radiant power	I _F = 100 mA, t _p = 20 ms	фе	-	50	-	mW
Temperature coefficient of radiant power	I _F = 100 mA	TKφ _e	-	-0.35	-	%/K
Angle of half intensity		φ	-	± 60	-	deg
Peak wavelength	I _F = 100 mA	λ _p	840	850	870	nm
Spectral bandwidth	I _F = 30 mA	Δλ	-	30	-	nm
Temperature coefficient of λ_p	I _F = 30 mA	TK _{λp}	-	0.25	-	nm/K
Rise time	$I_F = 100 \text{ mA}, 20 \% \text{ to } 80 \%$	t _r	-	10	-	ns
Fall time	$I_F = 100 \text{ mA}, 20 \% \text{ to } 80 \%$	t _f	-	10	-	ns
Virtual source diameter		d	-	0.5	_	mm

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Document Number: 84272

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

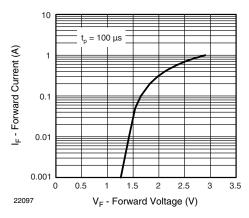


Fig. 3 - Forward Current vs. Forward Voltage

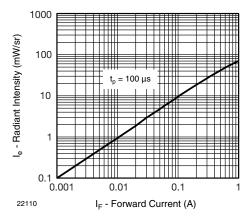


Fig. 4 - Radiant Intensity vs. Forward Current

REFLOW SOLDER PROFILE

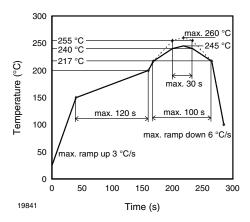


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

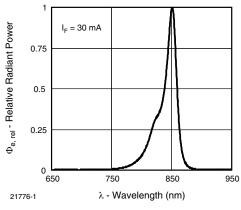


Fig. 5 - Relative Radiant Power vs. Wavelength

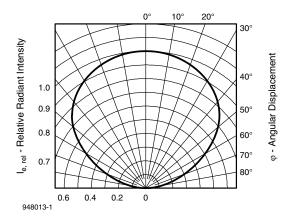


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 4

Floor life: 72 h

Conditions: T_{amb} < 30 °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

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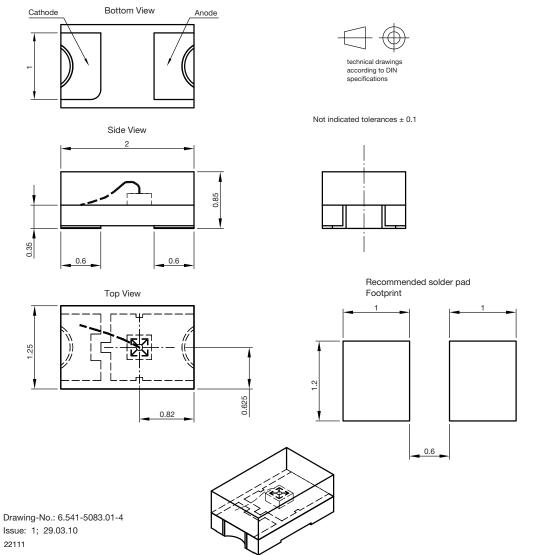
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PACKAGE DIMENSIONS in millimeters



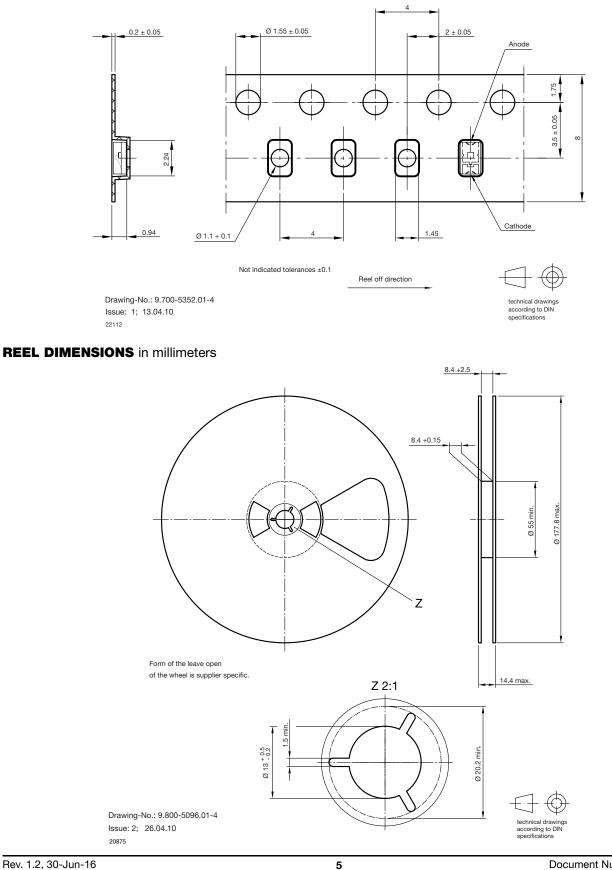
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