MOSFET – Power, N-Channel, SUPERFET III, FRFET

650 V, 36 A, 95 mΩ

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

Features

- 700 V @ T_J = 150°C
- Typ. $R_{DS(on)} = 82 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Qg = 66 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 569 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

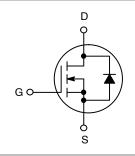
- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar

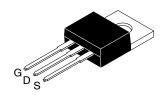


ON Semiconductor®

www.onsemi.com

V _{DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	95 mΩ @ 10 V	36 A





TO-220 CASE 340AT

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

kK = Lot

NTP095N65S3HF = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Parameter	Value	Unit			
V_{DSS}	Drain to Source Voltage			to Source Voltage		V
V_{GSS}	Gate to Source Voltage - DC	/oltage – DC		V		
		- AC (f > 1 Hz)	±30			
I _D	Drain Current	– Continuous (T _C = 25°C)	36	Α		
		- Continuous (T _C = 100°C)				
I _{DM}	Drain Current	- Pulsed (Note 1)	90	Α		
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	440	mJ			
I _{AS}	Avalanche Current (Note 2)		4.6	Α		
E _{AR}	Repetitive Avalanche Energy (Note 1)		2.72	mJ		
dv/dt	MOSFET dv/dt		100	V/ns		
	Peak Diode Recovery dv/dt (Note 3)	50				
P_{D}	Power Dissipation	(T _C = 25°C)	272	W		
		- Derate Above 25°C	2.176	W/°C		
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C		
TL	Maximum Lead Temperature for Soldering, 1/8"	300	°C			

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse–width limited by maximum junction temperature. 2. $I_{AS} = 4.6 \text{ A}$, $R_{G} = 25 \Omega$, starting $T_{J} = 25^{\circ}\text{C}$. 3. $I_{SD} \le 18 \text{ A}$, $\text{di/dt} \le 200 \text{ A/µs}$, $V_{DD} \le 400 \text{ V}$, starting $T_{J} = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ hetaJC}$	Thermal Resistance, Junction to Case, Max.	0.46	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTP095N65S3HF	NTP095N65S3HF	TO-220	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS		•		•	L.
BV _{DSS} Drain to Source Breakdown Voltage		$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	650			V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 15 mA, Referenced to 25°C		0.63		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V			10	μΑ
		V _{DS} = 520 V, T _C = 125°C		97		
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA
ON CHARACTE	ERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.86 \text{ mA}$	3.0		5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 18 A		82	95	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 18 A		22		S
DYNAMIC CHA	RACTERISTICS					
C _{iss}	Input Capacitance			2930		pF
C _{oss}	Output Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		61		pF
C _{oss(eff.)}	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		569		pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		110		pF
Q _{g(tot)}	Total Gate Charge at 10V	V _{DS} = 400 V, I _D = 18 A, V _{GS} = 10 V (Note 4)		66		nC
Q _{gs}	Gate to Source Gate Charge			21		nC
Q_{gd}	Gate to Drain "Miller" Charge	(1.13.3.1)		25		nC
ESR	Equivalent Series Resistance	f = 1 MHz		2.4		Ω
WITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time			28		ns
t _r	Turn-On Rise Time	$V_{DD} = 400 \text{ V}, I_D = 18 \text{ A},$		28		ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_g = 4.7 \Omega$ (Note 4)		72		ns
t _f	Turn-Off Fall Time			24		ns
SOURCE-DRAI	N DIODE CHARACTERISTICS					
I _S	Maximum Continuous Source to Drain Diode Forward Current				36	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current				90	Α
V_{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 18 A			1.3	V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 18 A,		106		ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$		414		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL CHARACTERISTICS

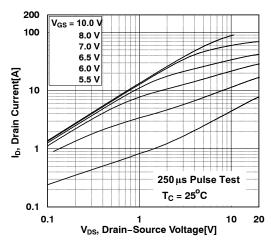


Figure 1. On-Region Characteristics

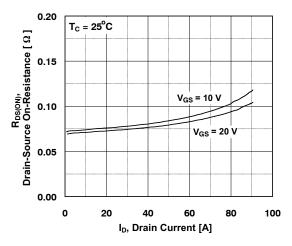


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

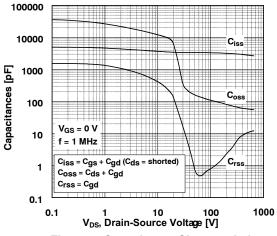


Figure 5. Capacitance Characteristics

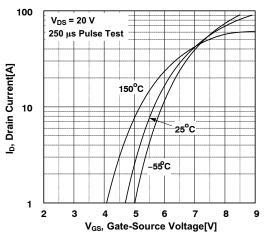


Figure 2. Transfer Characteristics

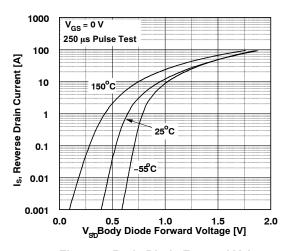


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

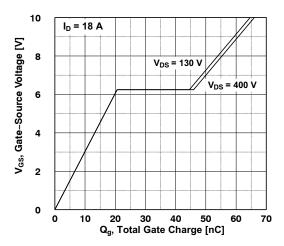


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS

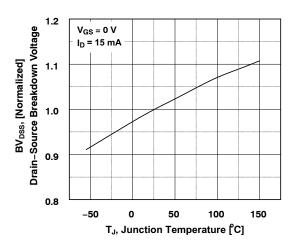


Figure 7. Breakdown Voltage Variation vs. Temperature

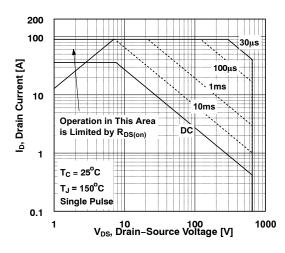


Figure 9. Maximum Safe Operating Area

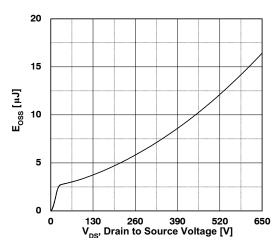


Figure 11. Eoss vs. Drain-to-Source Voltage

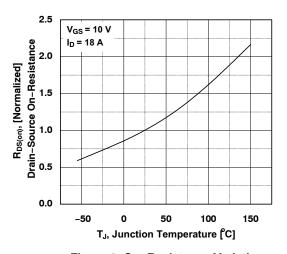


Figure 8. On–Resistance Variation vs. Temperature

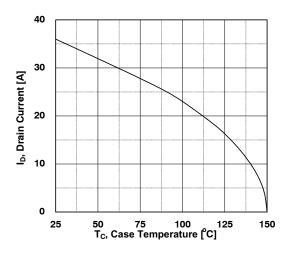


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL CHARACTERISTICS

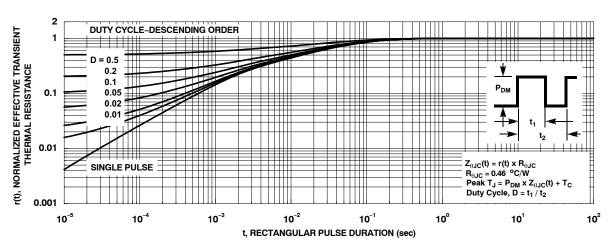


Figure 12. Transient Thermal Response Curve

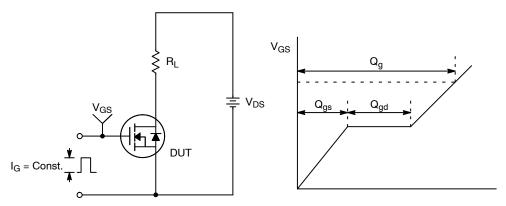


Figure 13. Gate Charge Test Circuit & Waveform

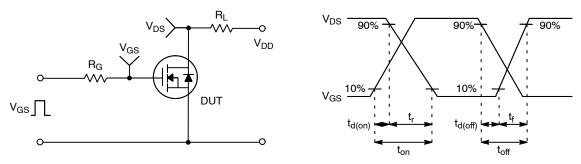


Figure 14. Resistive Switching Test Circuit & Waveforms

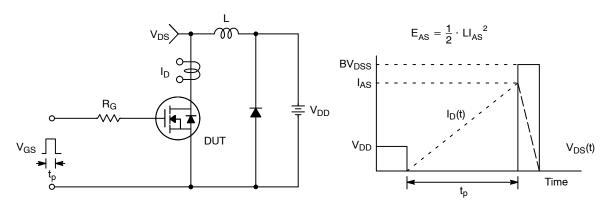
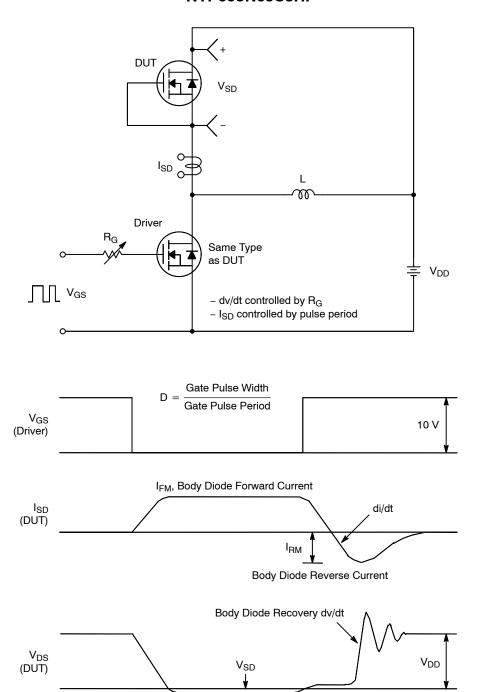


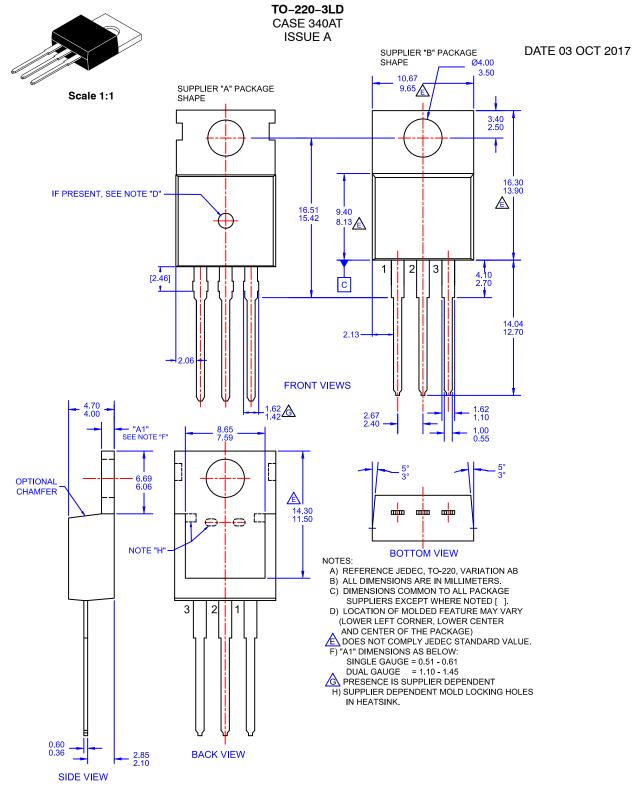
Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms



Forward Voltage Drop

Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Body Diode



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