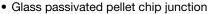


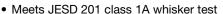
High Voltage, Input Rectifier Diode, 10 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	10 A			
V _R	1200 V			
V _F at I _F	1.1 V			
I _{FSM}	160 A			
T _J max.	150 °C			
Package	2L TO-220AC			
Circuit configuration	Single			

FEATURES







FREE

Flexible solution for reliable AC power rectification

rectification

- High surge, low V_F rugged blocking diode for DC charging stations
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- On-board and off-board EV/HEV battery chargers
- · Input rectification

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS						
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W						

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Sinusoidal waveform	10	A		
V_{RRM}		1200	V		
I _{FSM}		160	Α		
V _F	10 A, T _J = 25 °C	1.1	V		
TJ		-40 to +150	°C		

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA
VS-10ETS12THM3	1200	1300	0.5

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	10			
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	135	Α		
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	160			
Manian 124 for a forming	l ² t	10 ms sine pulse, rated V _{RRM} applied	91	A ² s		
Maximum I ² t for fusing		10 ms sine pulse, no voltage reapplied 130		A-S		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	1300	A ² √s		



ELECTRICAL SPECIFICATIONS						
PARAMETER SYMBOL TEST CONDITIONS VALUES UN						
Maximum forward voltage drop	V_{FM}	10 A, T _J = 25 °C		1.1	V	
Forward slope resistance	r _t	T _{.1} = 150 °C	20	mΩ		
Threshold voltage	V _{F(TO)}	1j=150 C	0.82	V		
Maximum reverse leakage current	1	T _J = 25 °C		0.05	m ^	
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	V _R = Rated V _{RRM}	0.50	mA mA	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5		
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA}		62	°C/W	
Soldering temperature	T _S		240	°C	
Approximate weight			2	g	
Approximate weight			0.07	oz.	
Marking device		Case style 2L TO-220AC	10ETS	312TH	

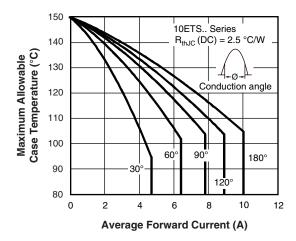


Fig. 1 - Current Rating Characteristics

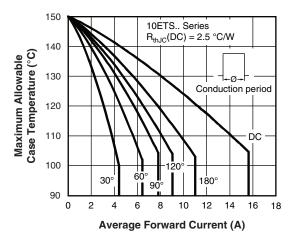


Fig. 2 - Current Rating Characteristics

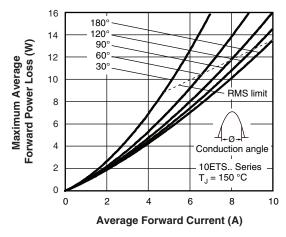


Fig. 3 - Forward Power Loss Characteristics

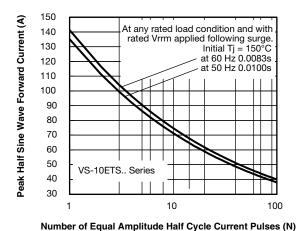


Fig. 5 - Maximum Non-Repetitive Surge Current

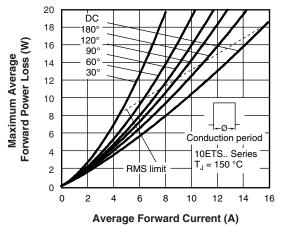


Fig. 4 - Forward Power Loss Characteristics

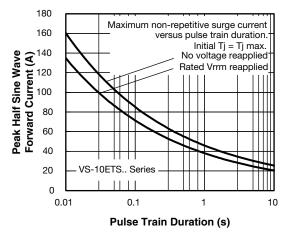


Fig. 6 - Maximum Non-Repetitive Surge Current

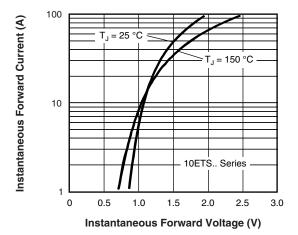


Fig. 7 - Forward Voltage Drop Characteristics

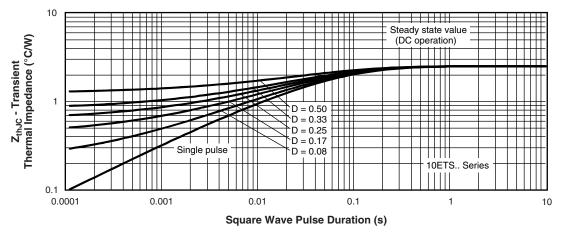
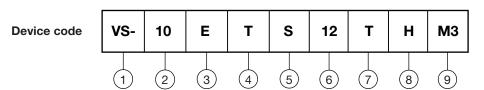


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



1 - Vishay Semiconductors product

2 - Current rating (10 = 10 A)

3 - Circuit configuration:

E = 2L TO-220AC

4 - Package:

T = TO-220

5 - Type of silicon:

S = standard recovery rectifier

6 - Voltage code x 100 = V_{RRM} ------- 12 = 1200 V

7 - None = TO-220AB

• T = true pin TO-220

8 - H = AEC-Q101 qualified

9 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

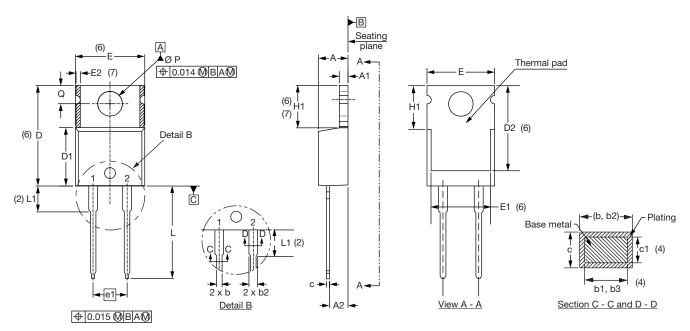
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-10ETS12THM3	50	1000	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?96069		
Part marking information	www.vishay.com/doc?95391		



2L TO-220AC

DIMENSIONS in millimeters and inches



CVMDOL	SYMBOL MILLIMETERS INCHES		NOTES		
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
e1	4.88	5.28	0.192	0.208	
H1	5.84	6.86	0.230	0.270	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC® TO-220, except D2, where JEDEC® minimum is 0.480"



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Vishay

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