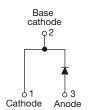


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Vishay Semiconductors

High Performance Schottky Rectifier, 15 A





TO-220AC 2L

PRIMARY CHARACTERISTICS									
I _{F(AV)} 15 A									
V _R	60 V								
V _F at I _F	0.56 V								
I _{RM} typ.	45 mA at 125 °C								
T _J max.	150 °C								
E _{AS}	6 mJ								
Package	2L TO-220AC								
Circuit configuration	Single								

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-15TQ060... Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES UNIT									
I _{F(AV)}	Rectangular waveform	15	Α						
V _{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	1000	Α						
V _F	15 A _{pk} , T _J = 125 °C	0.56	V						
T _J	Range	-55 to +150	°C						

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-15TQ060-M3 UNITS							
Maximum DC reverse voltage	V_R	60	V				
Maximum working peak reverse voltage	V_{RWM}	60					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 104 °C	15					
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated	1000	Α			
surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	260				
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH		6	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.50	А			

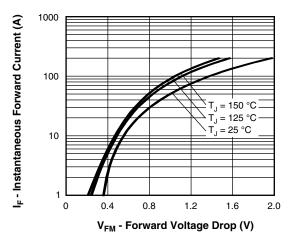


ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		15 A	T _{.1} = 25 °C	0.62				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.82	V			
See fig. 1	VFM (1)	15 A	T.ı = 125 °C	0.56				
		30 A	- IJ = 125 G	0.71				
Maximum various la alcaga accument	I _{RM} (1)	T _J = 25 °C	V Datad V	0.80	mA			
Maximum reverse leakage current		T _J = 125 °C	V _R = Rated V _R	160				
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	45	mA			
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) 25 °C		720	pF			
Typical series inductance	L _S	Measured lead to lead 5 n	8	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C			
Maximum thermal resistance, junction to case		R_{thJC}	DC operation See fig. 4	3.25	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	- C/VV			
Annyayimata wajaht				2	g			
Approximate weight				0.07	OZ.			
Mounting torque	ninimum			6 (5)	kgf · cm			
Mounting torque n	naximum			12 (10)	(lbf ⋅ in)			
Marking device			Case style 2L TO-220AC (JEDEC®)	15T0	2060			



1000 I_R - Reverse Current (mA) 100 T_J = 125 °C 10 T₁ = 100 °C T_{.1} = 75 °C 0.1 = 50 °C 0.01 $T_{\rm c} = 25 \, ^{\circ}\text{C}$ 0.001 30 10 20 40 50 60 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

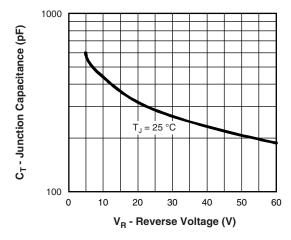


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

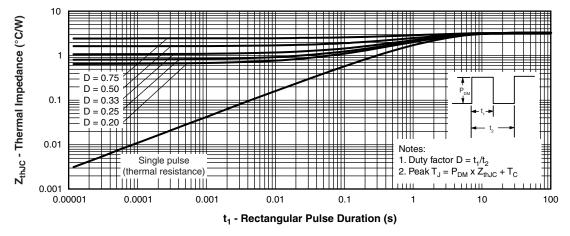


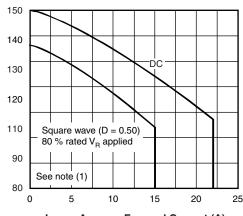
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



Allowable Case Temperature (°C)

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I_{F(AV)} - Average Forward Current (A)

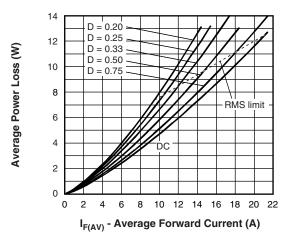


Fig. 6 - Forward Power Loss Characteristics

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

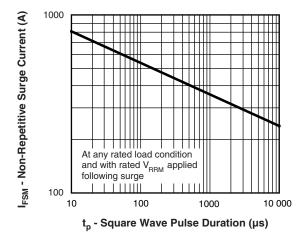


Fig. 7 - Maximum Non-Repetitive Surge Current

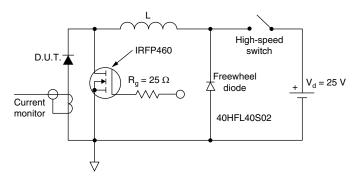


Fig. 8 - Unclamped Inductive Test Circuit

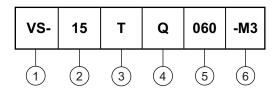
Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}$; $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (15 = 15 A)

- Package:

T = TO-220

4 - Schottky "Q" series

Voltage rating (060 = 60 V)

6 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

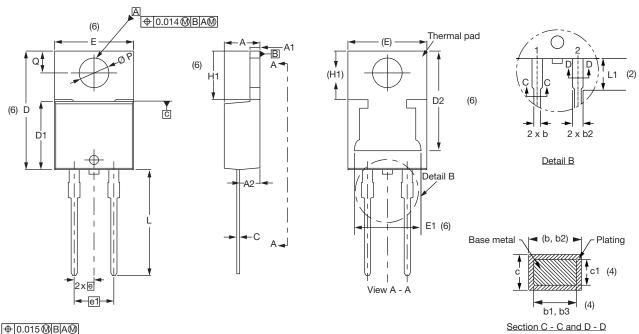
ORDERING INFORMATION (Example)									
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION									
VS-15TQ060-M3	50	Antistatic plastic tube							

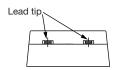
LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?96156						
Part marking information	www.vishay.com/doc?95391						
SPICE model	www.vishay.com/doc?95600						



2L TO-220AC

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIM	IETERS	INC	INCHES		NOTES		MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355				•	•			

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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