MOSFET - N-Channel, SUPREMOS®

600 V, 47 A, 62 m Ω

FCH47N60N

Description

The SUPREMOS MOSFET is ON Semiconductor's next generation of high voltage super-junction (SJ) technology employing a deep trench filling process that differentiates it from the conventional SJ MOSFETs. This advanced technology and precise process control provides lowest Rsp on–resistance, superior switching performance and ruggedness. SUPREMOS MOSFET is suitable for high frequency switching power converter applications such as PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.

Features

- $650 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- $R_{DS(on)} = 51.5 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 23.5 \text{ A}$
- Ultra Low Gate Charge (Typ. Q_g = 115 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 511 pF)
- 100% Avalanche Tested
- This Device is Pb-Free and is RoHS Compliant

Applications

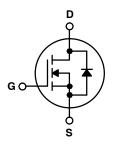
- Solar Inverter
- AC-DC Power Supply



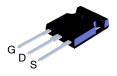
ON Semiconductor®

www.onsemi.com

| V _{DS} | R _{DS(ON)} MAX | I _D MAX | |
|-----------------|-------------------------|--------------------|--|
| 600 V | 62 mΩ @ 10 V | 47 A | |

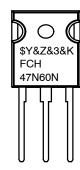


N-CHANNEL MOSFET



TO-247-3LD CASE 340CK

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code

&3 = Numeric Date Code &K = Lot Code

FCH47N60N = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C unless otherwise noted)

| Symbol | | Parameter | Value | Unit |
|-----------------------------------|---|---------------------------------------|--------------|------|
| V_{DSS} | Drain to Source Voltage | | 600 | V |
| V_{GSS} | Gate to Source Voltage | | ±30 | V |
| I _D | Drain Current | – Continuous (T _C = 25°C) | 47 | Α |
| | | – Continuous (T _C = 100°C) | 29.7 | 1 |
| I _{DM} | Drain Current | - Pulsed (Note 1) | 141 | Α |
| E _{AS} | Single Pulsed Avalanche Energy (Note | 3068 | mJ | |
| I _{AR} | Avalanche Current (Note 1) | 15.7 | Α | |
| E _{AR} | Repetitive Avalanche Energy (Note 1) | | 3.7 | mJ |
| dv/dt | MOSFET dv/dt | | 100 | V/ns |
| | Peak Diode Recovery dv/dt (Note 3) | 20 | 1 | |
| P_{D} | Power Dissipation | (T _C = 25°C) | 368 | W |
| | | – Derate above 25°C | 2.94 | W/°C |
| T _J , T _{STG} | Operating and Storage Temperature Range | | -55 to + 150 | °C |
| TL | Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Second | | 300 | °C |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. $I_{AS} = 15.7 \text{ A}$, $R_{G} = 25 \Omega$, starting $T_{J} = 25 ^{\circ}\text{C}$ 3. $I_{SD} \le 47 \text{ A}$, $di/dt \le 200 \text{ A}/\mu\text{s}$, $V_{DD} \le 380 \text{ V}$, starting $T_{J} = 25 ^{\circ}\text{C}$

PACKAGE MARKING AND ORDERING INFORMATION

| Part Number | Top Mark | Package | Package Method | Reel Size | Tape Width | Quantity |
|-------------|-----------|------------|----------------|-----------|------------|----------|
| FCH47N60N | FCH47N60N | TO-247-3LD | Tube | N/A | N/A | 30 Units |

THERMAL CHARACTERISTICS

| Symbol | Parameter | Value | Unit |
|-----------------|---|-------|------|
| $R_{	heta JC}$ | Thermal Resistance, Junction to Case, Max. | 0.34 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient, Max. | 40 | |

$\textbf{ELECTRICAL CHARACTERISTICS} \; (T_C = 25^{\circ}\text{C unless otherwise noted})$

| Symbol | Parameter | Test Condition | Min. | Тур. | Max. | Unit |
|---|--|--|------|------|------|------|
| OFF CHARA | ACTERISTICS | | | | | |
| BV _{DSS} | Drain to Source Breakdown Voltage | I _D = 1 mA, V _{GS} = 0 V, T _C = 25°C | 600 | - | - | V |
| ΔBV _{DSS} / ΔT _J | Breakdown Voltage Temperature Coefficient | I _D = 1 mA, Referenced to 25°C | - | 0.78 | - | V/°C |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} = 480 V, V _{GS} = 0 V | _ | - | 10 | μΑ |
| | | V _{DS} = 480 V, V _{GS} = 0 V, T _C = 125°C | _ | - | 100 | |
| I _{GSS} | Gate to Body Leakage Current | V _{GS} = +30 V, V _{DS} = 0 V | _ | - | ±100 | nA |
| ON CHARA | CTERISTICS | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | $V_{GS} = V_{DS}$, $I_D = 250 \text{ mA}$ | 2 | - | 4 | ٧ |
| R _{DS(on)} | Static Drain to Source On Resistance | V _{GS} = 10 V, I _D = 23.5 A | - | 51.5 | 62.0 | mΩ |
| 9FS | Forward Transconductance | V _{DS} = 40 V, I _D = 23.5 A | - | 56 | - | S |
| DYNAMIC C | HARACTERISTICS | | • | • | | |
| C _{iss} | Input Capacitance | V _{DS} = 100 V, V _{GS} = 0 V, | - | 5037 | 6700 | pF |
| C _{oss} | Output Capacitance | f = 1 MHz | _ | 200 | 270 | pF |
| C _{rss} | Reverse Transfer Capacitance | 1 | _ | 2.5 | 4.0 | pF |
| C _{oss} | Output Capacitance | V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz | _ | 108 | - | pF |
| C _{oss(eff.)} | Effective Output Capacitance | V _{DS} = 0 V to 380 V, V _{GS} = 0 V | - | 511 | - | pF |
| Q _{g(tot)} | Total Gate Charge at 10 V | V _{DS} = 380 V, I _D = 23.5 A, | _ | 115 | 151 | nC |
| Q _{gs} | Gate to Source Gate Charge | V _{GS} = 10 V (Note 4) | _ | 21 | - | nC |
| Q _{gd} | Gate to Drain "Miller" Charge | 1` | _ | 34 | - | nC |
| ESR | Equivalent Series Resistance (G-S) | f = 1 MHz | - | 0.9 | _ | Ω |
| SWITCHING | CHARACTERISTICS | | • | • | | |
| t _{d(on)} | Turn-On Delay Time | $V_{DD} = 380 \text{ V}, I_D = 23.5 \text{ A},$ | _ | 11 | 32 | ns |
| t _r | Turn-On Rise Time | $R_G = 4.7 \Omega$ (Note 4) | _ | 9 | 28 | ns |
| t _{d(off)} | Turn-Off Delay Time | 1` | _ | 135 | 280 | ns |
| t _f | Turn-Off Fall Time | 1 | _ | 22 | 54 | ns |
| DRAIN-SOU | RCE DIODE CHARACTERISTICS | | 1 | | | |
| I _S | Maximum Continuous Drain to Source Diode Forward Current | | - | - | 47 | Α |
| I _{SM} | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 141 | Α |
| V _{SD} | Drain to Source Diode Forward Voltage | V _{GS} = 0 V, I _{SD} = 23.5 A | _ | - | 1.2 | V |
| t _{rr} | Reverse Recovery Time | V _{GS} = 0 V, I _{SD} = 23.5 A, | _ | 495 | - | ns |
| Q _{rr} | Reverse Recovery Charge | $dI_{F}/dt = 100 \text{ A/ms}$ | _ | 12 | - | μC |
| | | trical Characteristics for the listed test of | | · | | |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially Independent of Operating Temperature Typical Characteristics.

TYPICAL CHARACTERISTICS

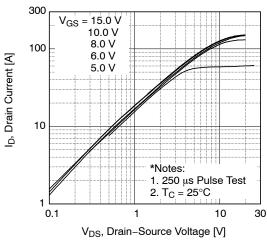


Figure 1. On-Region Characteristics

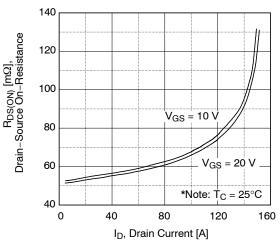


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

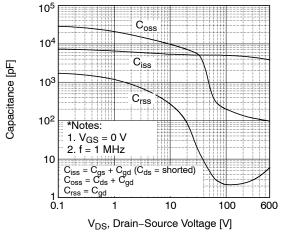


Figure 5. Capacitance Characteristics

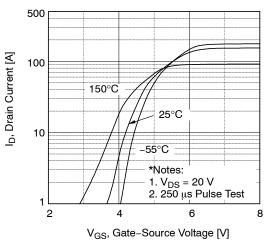


Figure 2. Transfer Characteristics

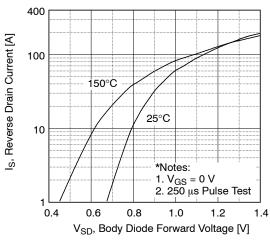


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

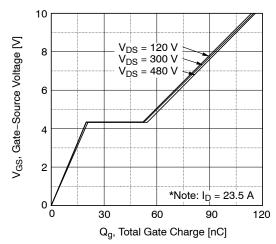


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS (continued)

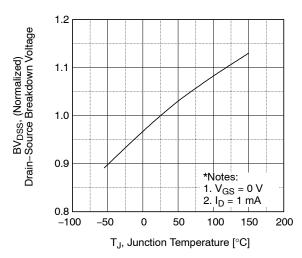


Figure 7. Breakdown Voltage Variation vs. Temperature

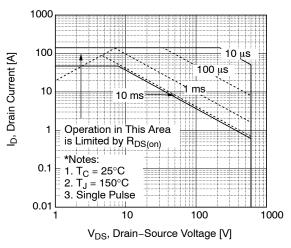
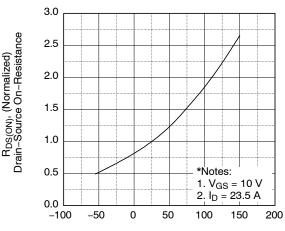


Figure 9. Maximum Safe Operating Area



T_J, Junction Temperature [°C]

Figure 8. On–Resistance Variation vs. Temperature

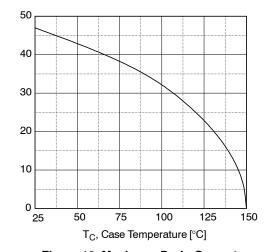


Figure 10. Maximum Drain Current vs. Case Temperature

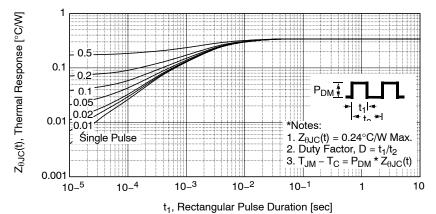


Figure 11. Transient Thermal Response Curve

ID, Drain Current [A]

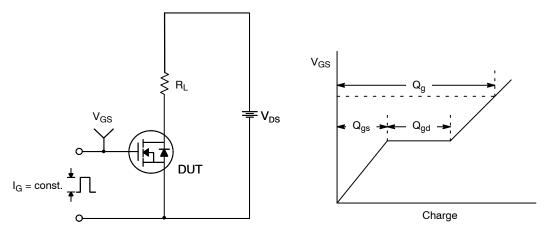


Figure 12. Gate Charge Test Circuit & Waveform

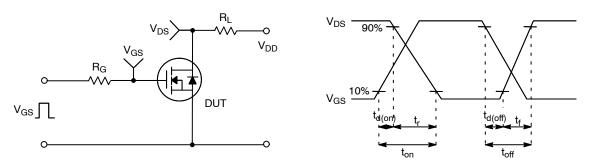


Figure 13. Resistive Switching Test Circuit & Waveforms

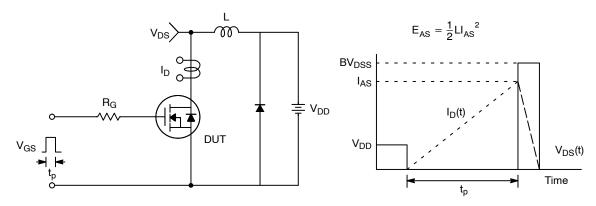


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

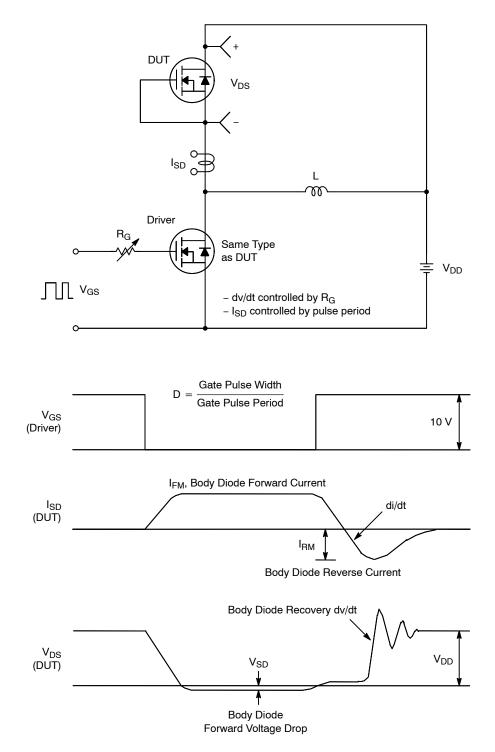
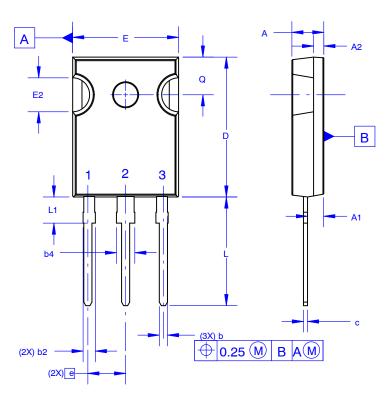


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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TO-247-3LD SHORT LEAD

CASE 340CK ISSUE A





- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

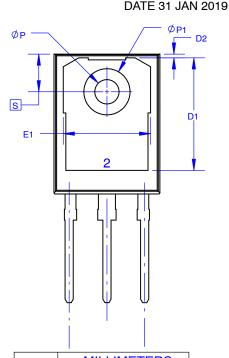
A = Assembly Location

Y = Year

WW = Work Week

ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



| DIM | MILLIMETERS | | | | |
|-------------|-------------|-------|-------|--|--|
| DIIVI | MIN | NOM | MAX | | |
| Α | 4.58 | 4.70 | 4.82 | | |
| A1 | 2.20 | 2.40 | 2.60 | | |
| A2 | 1.40 | 1.50 | 1.60 | | |
| b | 1.17 | 1.26 | 1.35 | | |
| b2 | 1.53 | 1.65 | 1.77 | | |
| b4 | 2.42 | 2.54 | 2.66 | | |
| С | 0.51 | 0.61 | 0.71 | | |
| D | 20.32 | 20.57 | 20.82 | | |
| D1 | 13.08 | ~ | ~ | | |
| D2 | 0.51 | 0.93 | 1.35 | | |
| E | 15.37 | 15.62 | 15.87 | | |
| E1 | 12.81 | ~ | ~ | | |
| E2 | 4.96 | 5.08 | 5.20 | | |
| е | ~ | 5.56 | ~ | | |
| L | 15.75 | 16.00 | 16.25 | | |
| L1 | 3.69 | 3.81 | 3.93 | | |
| ØΡ | 3.51 | 3.58 | 3.65 | | |
| Ø P1 | 6.60 | 6.80 | 7.00 | | |
| Q | 5.34 | 5.46 | 5.58 | | |
| S | 5.34 | 5.46 | 5.58 | | |

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|------------------|-----------------------|---|-------------|--|
| DESCRIPTION: | TO-247-3LD SHORT LEAD | | PAGE 1 OF 1 | |

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