

QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 16:8 MULTIPLEXER

IDTQS3390

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- · 16:8 multiplexer function with zero delay
- 5Ω bidirectional switches connect inputs to outputs
- · Zero propagation delay, zero ground bounce
- Low power CMOS proprietary technology
- · Undershoot clamp diodes on all switch and control inputs
- · Direct bidirectional connection for mux, demux
- · Available in QSOP package

APPLICATIONS:

- · Video, audio, graphics switching, muxing
- · Hot-swapping, hot-docking
- · Voltage translation (5V to 3.3V)

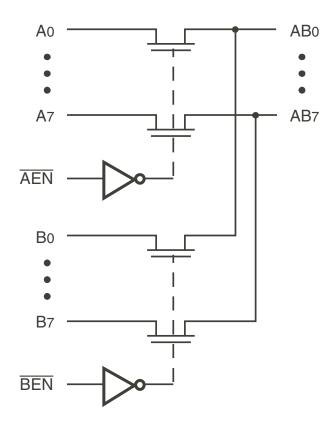
DESCRIPTION:

The QS3390 provides a 16:8 multiplexer logic switch. The low ON resistance (5Ω) of the QS3390 allows inputs to be connected to the outputs without adding propagation delay and without generating additional ground bounce noise.

Mux/Demux devices provide an order of magnitude faster speed than equivalent logic devices.

The QS3390 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



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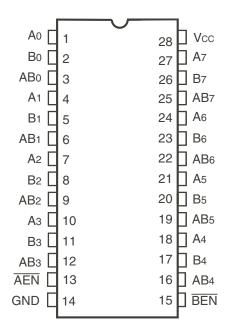
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INDUSTRIAL TEMPERATURE RANGE

OCTOBER 2014

DSC-575:

PIN CONFIGURATION



QSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	–0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	-0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage VIN	-0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V
lout	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (T _A = 85°C)	0.5	W
Tstg	Storage Temperature	-65 to +150	°C

NOTES:

- 1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc terminals.
- 3. All terminals except Vcc .

CAPACITANCE (TA = +25°C, f = 1MHz, VIN = 0V, VOUT = 0V)

Pins	Тур.	Max. ⁽¹⁾	Unit	
Control Inputs	4	5	рF	
Quickswitch Channels	Demux	5	7	рF
(Switch OFF)	Mux	9	10	

NOTE:

PIN DESCRIPTION

Pin Names	I/O	Description	
A0 - A7	I/O	Bus A	
B0 - B7	I/O	Bus B	
ĀĒN, BĒN	I	Bus Switch Enable	
AB0 - AB7	I/O	Bus AB	

FUNCTION TABLE(1)

ĀĒÑ	BEN	A0 - A7	B0 - B7	Function
Н	Н	Off	Off	Disconnect
L	Н	On	Off	A to AB
Н	L	Off	On	B to AB
L	L	On	On	A, B to AB

NOTE:

H = HIGH Voltage Level
 L = LOW Voltage Level

^{1.} This parameter is guaranteed but not production tested.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

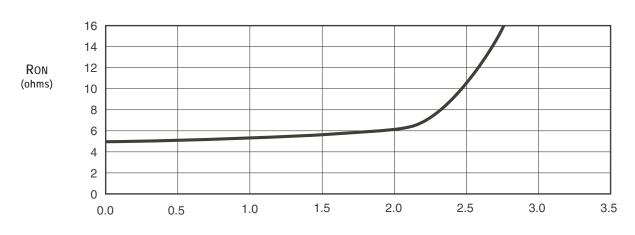
Industrial: TA = -40°C to +85°C, Vcc = $5V \pm 5\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
VIH	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	_	_	V
VIL	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	_	_	0.8	V
lin	Input Leakage Current (Control Inputs)	0V ≤ Vin ≤ Vcc	_	_	±1	μΑ
loz	Off-State Current (Hi-Z)	0V ≤ Vout ≤ Vcc	_	_	±1	μA
Ron	Switch ON Resistance	Vcc = Min., VIN = 0V, ION = 30mA	_	5	7	Ω
		Vcc = Min., VIN = 2.4V, ION = 15mA	_	10	15	
VP	Pass Voltage ⁽²⁾	VIN = Vcc = 5V, lout = -5μA	3.7	4	4.2	V

NOTES:

- 1. Typical values are at Vcc = 5V and TA = 25°C.
- 2. Pass voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs VIN AT Vcc = 5V



VIN (Volts)

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
Iccq	Quiescent Power Supply Current	Vcc = Max., Vin = GND or Vcc, f = 0	3	μΑ
Δlcc	Power Supply Current per Input HIGH(2)	Vcc = Max., Vin = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽³⁾	Vcc = Max., A, B, and AB Pins Open, Control Inputs Toggling @ 50% Duty Cycle	0.25	mA/MHz

NOTES:

- 1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- 2. Per TTL-driven input (VIN = 3.4V, control inputs only). A, B, and AB pins do not contribute to Δlcc.
- 3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A, B, and AB inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40$ °C to +85°C, $V_{CC} = 5V \pm 5\%$

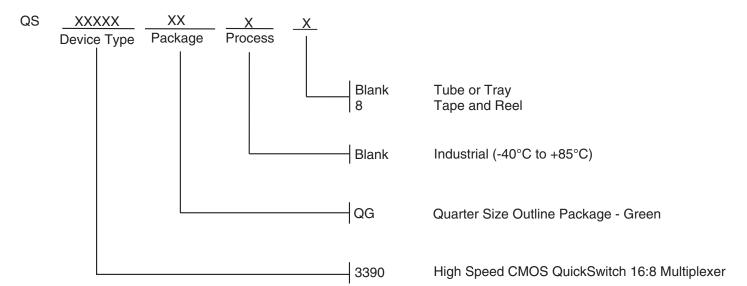
CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
t PLH	Data Propagation Delay ⁽²⁾	_	_	0.25 ⁽³⁾	ns
t PHL	A, B, to/from AB				
tpzL	Switch Turn-On Delay	1.5	_	6.5	ns
t PZH	AEN, BEN to A, B, AB				
tPLZ	Switch Turn-Off Delay ⁽²⁾	1.5	_	5.5	ns
t PHZ	AEN, BEN to A, B, AB				

NOTES:

- 1. Minimums are guaranteed but not production tested.
- 2. This parameter is guaranteed but not production tested.
- 3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns at CL = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

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